

# SUPER<sup>®</sup>

X9SCi-LN4  
X9SCi-LN4F  
X9SCA  
X9SCA-F

## USER'S MANUAL

Revision 1.0c

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## Preface

This manual is written for system integrators, PC technicians and knowledgeable PC users. It provides information for the installation and use of the **SUPER**® X9SCi-LN4/X9SCi-LN4F/X9SCA/X9SCA-F motherboard.

### About This Motherboard

The **SUPER**® X9SCi-LN4/X9SCi-LN4F/X9SCA/X9SCA-F supports a single Intel® Xeon E3-1200 series, 2nd generation Intel Core® i3, Pentium®, Celeron® processor in an LGA 1155 socket. With the Intel® C204 chipset built in, the X9SCi-LN4/X9SCA series motherboard offers substantial enhancement in system performance and storage capability for next generation high end server platforms\* in a sleek package. Please refer to our website (<http://www.supermicro.com/products/>) for processor and memory support updates. This product is intended to be installed and serviced by professional technicians.

\*The X9SCi-LN4 series motherboard supports 1U server platforms, and the X9SCA supports 3U/4U platforms.

### Manual Organization

**Chapter 1** describes the features, specifications and performance of the motherboard, and provides detailed information on the Intel C204 chipset.

**Chapter 2** provides hardware installation instructions. Read this chapter when installing the processor, memory modules and other hardware components into the system. If you encounter any problems, see **Chapter 3**, which describes troubleshooting procedures for video, memory and system setup stored in the CMOS.

**Chapter 4** includes an introduction to the BIOS, and provides detailed information on running the CMOS Setup utility.

**Appendix A** provides BIOS Error Beep Codes.

**Appendix B** lists software program installation instructions.

**Appendix C** contains the UEFI BIOS Recovery instructions.

## Conventions Used in the Manual:

Special attention should be given to the following symbols for proper installation and to prevent damage done to the components or injury to yourself:



**Danger/Caution:** Instructions to be strictly followed to prevent catastrophic system failure or to avoid bodily injury



**Warning:** Critical information to prevent damage to the components or data loss.



**Important:** Important information given to ensure proper system installation or to relay safety precautions.



**Note:** Additional Information given to differentiate various models or provides information for correct system setup.



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# Chapter 1

## Introduction

### 1-1 Overview

#### Checklist

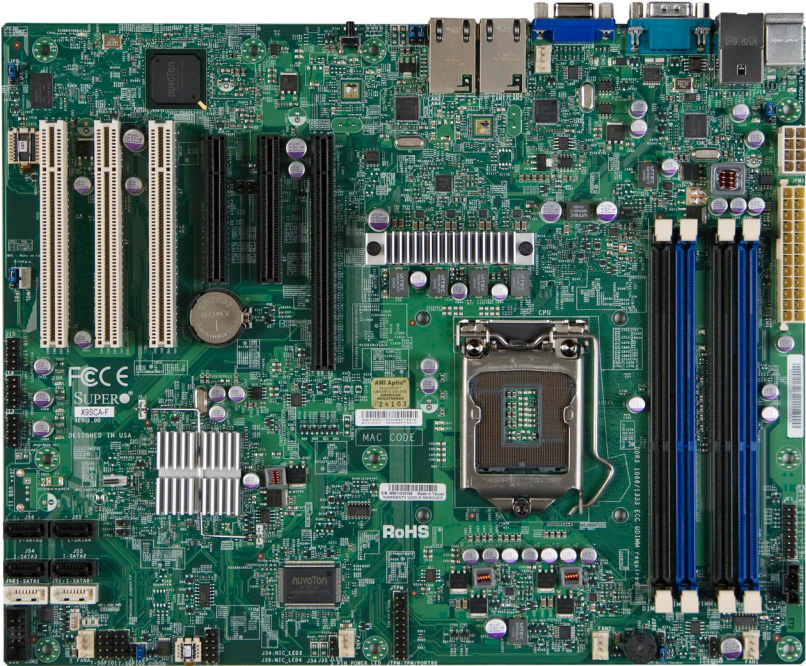
Congratulations on purchasing your computer motherboard from an acknowledged leader in the industry. Supermicro boards are designed with the utmost attention to detail to provide you with the highest standards in quality and performance.

Please check that the following items have all been included with your motherboard. If anything listed here is damaged or missing, contact your retailer.

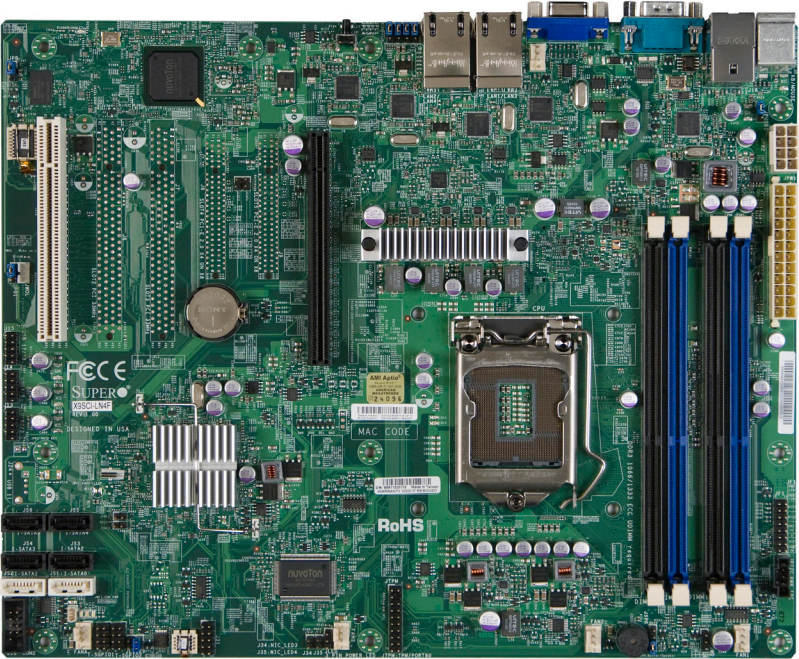
The following items are included in the retail box.

- One (1) Supermicro Mainboard
- Six (6) SATA cables
- One (1) I/O shield
- One (1) Supermicro CD containing drivers and utilities
- One (1) User's Manual

SUPER X9SCA Series Motherboard Image

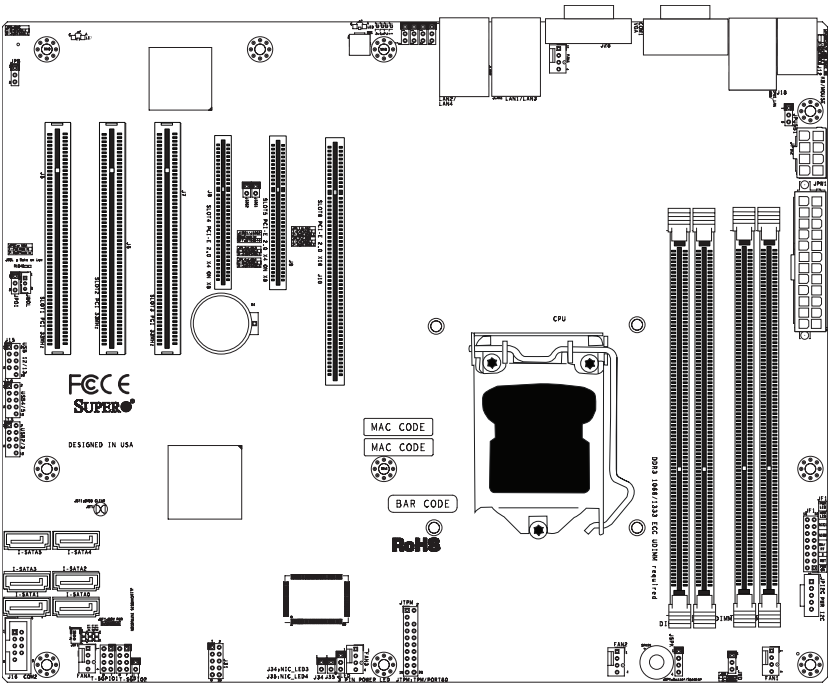


**Note:** All graphics shown in this manual were based upon the latest PCB Revision available at the time of publishing of the manual. The motherboard you've received may or may not look exactly the same as the graphics shown in this manual.

**SUPER** X9Sci-LN4 Series Motherboard Image

**Note:** All graphics shown in this manual were based upon the latest PCB Revision available at the time of publishing of the manual. The motherboard you've received may or may not look exactly the same as the graphics shown in this manual.

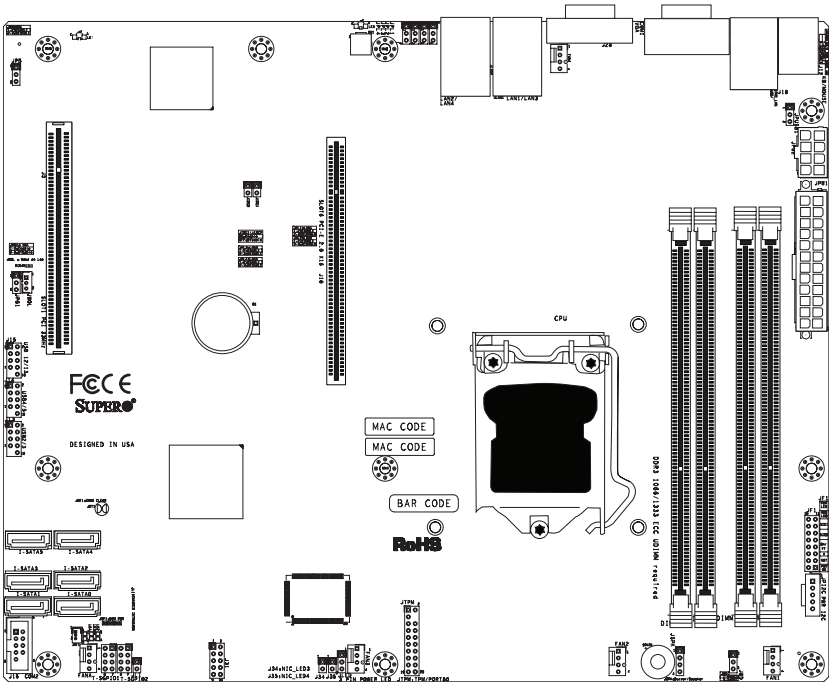
Motherboard Layout (X9SCA Series)



**Important Notes to the User**

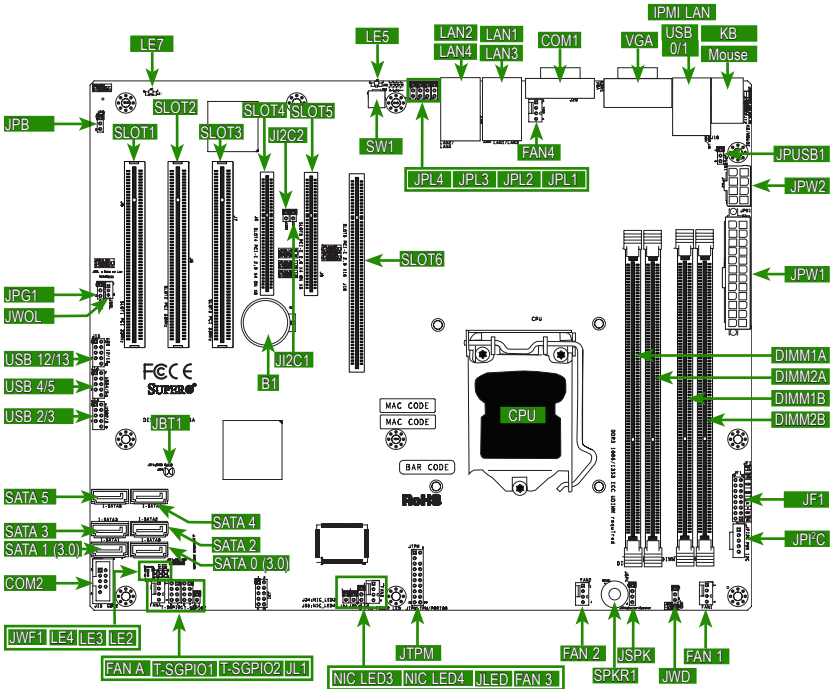
- See Chapter 2 for detailed information on jumpers, I/O ports and JF1 front panel connections.
- "■" indicates the location of "Pin 1".
- Jumpers not indicated are for testing only.
- When LE2 (Onboard Power LED Indicator) is on, system power is on. Unplug the power cable before installing or removing any components.

## Motherboard Layout (X9Sci-LN4 Series)

**Important Notes to the User**

- See Chapter 2 for detailed information on jumpers, I/O ports and JF1 front panel connections.
- "■" indicates the location of "Pin 1".
- Jumpers not indicated are for testing only.
- When LE2 (Onboard Power LED Indicator) is on, system power is on. Unplug the power cable before installing or removing any components.

**X9SCi-LN4/X9SCi-LN4F/X9SCA/X9SCA-F Quick Reference**



**X9SCi-LN4/X9SCi-LN4F/X9SCA/X9SCA-F Jumpers**

| Jumper                                | Description                             | Default            |
|---------------------------------------|---|--------------------|
| JBT1                                  | CMOS Clear                              | (See Chpt. 2)      |
| JI <sup>2</sup> C1/JI <sup>2</sup> C2 | SMB to PCI Slots                        | (See Chpt. 2)      |
| JPB                                   | BMC Enable (X9SCi-LN4F, X9SCA-F Only)   | Pins 1-2 (Enabled) |
| JPG1                                  | Onboard VGA Enable                      | Pins 1-2 (Enabled) |
| JPL1/JPL2                             | LAN1/LAN2 Enable                        | Pins 1-2 (Enabled) |
| JPL3/JPL4                             | LAN3/LAN4 Enable (X9SCi-LN4/-LN4F only) | Pins 1-2 (Enabled) |
| JPU3B1                                | Backpanel USB 0/1 Wake-Up Enable        | Pins 1-2 (Enabled) |
| JWD                                   | Watch Dog Timer Enable                  | Pins 1-2 (Reset)   |

**X9SCi-LN4/X9SCi-LN4F/X9SCA/X9SCA-F LED Indicators**

| LED | Description                               | Color/State      | Status             |
|-----|---|------------------|--------------------|
| LE2 | Onboard Power On LED                      | Green: Solid on  | System On          |
| LE3 | Unsupported Memory Installed Indicator    | Yellow: Blinking | Unsupported Memory |
| LE4 | Onboard Standby PWR LED                   | Green: Solid On  | Standby Power On   |
| LE5 | UID (Unit ID) LED                         | Blue: Solid On   | UID: On            |
| LE7 | IPMI Heartbeat (X9SCi-LN4F, X9SCA-F Only) | Green: Blinking  | IPMI Normal        |

| <b>X9SCi-LN4/X9SCi-LN4F/X9SCA/X9SCA-F Headers/Connectors</b> |   |
|--|---|
| Connector  | Description   |
| B1   | Onboard Battery   |
| COM1, COM2   | COM1 Backpanel Serial Port, COM2 Serial Port Header                 |
| BIOS   | SPI BIOS  |
| Fans 1~4, Fan A  | System/CPU Fan Headers  |
| JF1  | Front Panel Control Header  |
| JL1  | Chassis Intrusion Header  |
| JLED1  | Power LED Indicator Header  |
| JPW1   | 24-pin ATX Main Power Connector (Required)                          |
| JPW2   | +12V 8-pin CPU power Connector (Required)                           |
| KB, Mouse  | Keyboard/Mouse Connectors   |
| LAN1/LAN2/LAN3/LAN4  | Gigabit (RJ45) Ports (LAN3/LAN4 is supported on the X9SCi-LN4 only) |
| IPMI LAN   | IPMI LAN (X9SCA-F/X8SCi-LN4F models only)                           |
| SATA 0/1   | Serial ATA 3.0 Ports 0/1 (6Mb/s)                                    |
| SATA 2~5   | Serial ATA 2.0 Ports 2~5 (3Mb/s)                                    |
| JPI <sup>2</sup> C   | PWR supply (I <sup>2</sup> C) System Management Bus                 |
| JSPK   | Speaker Header (Pins 3/4: Internal, 1~4:External)                   |
| JTPM   | Trusted Platform Module (TPM) Header                                |
| JWF1   | SATA DOM (Disk On Module) Power Connector                           |
| JWOL   | Wake On LAN Header  |
| SPKR1  | Internal Speaker/Buzzer   |
| T-SGPIO-1/2  | Serial Link General Purpose I/O 1/2 Headers (5V Gen1/Gen 2)         |
| USB0/1   | Backpanel USB 0/1   |
| USB2/3, USB4/5,<br>USB 12/13                                 | Front Accessible USB Connections (via 3 Headers)                    |
| VGA  | Onboard Video Port  |
| NIC LED3, NIC LED4   | LED Headers for LAN3 and LAN4 (X9SCi-LN4 Only)                      |
| DIMM 1A, 2A, 1B, 2B  | 1066/1333 DDR3 DIMM Slots (ECC, Unbuffered type is required)        |
| SW1  | Unit ID Switch to turn on UID LED (LE5)                             |
| Slot 1~3   | 33MHz PCI Slots (Slot 2/3 are not available on the X9SCi series)    |
| Slot 4~5   | PCI-Express Slots x4 in x8 (Not available on the X9SCi series)      |
| Slot 6   | PCI-Express Slot x16  |

## Motherboard Features

|                            |  |                       |
|----------------------------|--|-----------------------|
| <b>CPU</b>                 | Single Intel® Xeon E3-1200 series, 2nd generation Intel Core® i3, Pentium®, Celeron® processor in an LGA 1155 socket |                       |
| <b>Memory</b>              | Four (4) SDRAM slots support up to 32 GB of DDR3 Unbuffered, ECC 1333/1066 memory                                    |                       |
|                            | Supports dual-channel memory bus   |                       |
|                            | <b>DIMM sizes</b>  |                       |
|                            | UDIMM  | 1 GB, 2 GB, and 4GB   |
| <b>Chipset</b>             | Intel® C204 Standard   |                       |
| <b>Expansion Slots</b>     | <b>X9SCA/X9SCA-F Only</b>  |                       |
|                            | Three (3) 33MHz PCI Slots  |                       |
|                            | Two (2) PCI-Express 2.0 x4 in x8 Slots   |                       |
|                            | One (1) PCI-Express 2.0 x16 Slot   |                       |
|                            | <b>X9SCi-LN4/-LN4F Only</b>  |                       |
|                            | One (1) 33MHz PCI Slot   |                       |
|                            | One (1) PCI-Express 2.0 x16 Slot   |                       |
| <b>Integrated Graphics</b> | Matrox® G200eW   |                       |
| <b>Network Connections</b> | Two (2) Intel 82574L (Four for the X9SCi-LN4/-LN4F)  |                       |
|                            | Two (2) RJ-45 Rear IO Panel Connectors with Link and Activity LEDs, Four (4) for the X9SCi-LN4/-LN4F                 |                       |
|                            | Single Realtek RTL8201N PHY to support IPMI 2.0 LAN (X9SCi-LN4F/X9SCA-F Only)  |                       |
| <b>I/O Devices</b>         | <b>SATA Connections</b>  |                       |
|                            | SATA 3.0 Ports   | Two (2) (I-SATA 0/1)  |
|                            |  | RAID 0, 1 Support     |
|                            | SATA 2.0 Ports   | Four (4) (I-SATA 2~5) |
|                            |  | RAID 0, 1, 5, 10      |
|                            | <b>Integrated IPMI 2.0</b>   |                       |
|                            | IPMI 2.0 supported by the WPCM450 Server BMC (X9SCi-LN4F/X9SCA-F Only)   |                       |
|                            | <b>USB Devices</b>   |                       |
|                            | Two (2) USB ports on the rear I/O panel (USB 0/1)  |                       |
|                            | Six (6) USB header connectors for front access (USB 2/3, USB 4/5, USB 12/13)   |                       |



|                             |  |
|-----------------------------|--|
|                             | <b>Keyboard/Mouse</b><br>PS/2 Keyboard/Mouse ports on the I/O backpanel<br><b>Serial (COM) Ports</b><br>Two (2) Fast UART 16550 connections: one 9-pin RS-232 port (Backplane COM1 port) and one header (FP COM2)<br><b>Super I/O</b><br>Winbond Super I/O NCT6776F  |
| <b>BIOS</b>                 | 64 Mb SPI AMI BIOS® SM Flash BIOS<br>Play and Plug (PnP0, DMI 2.3, PCI 2.3, ACPI 1.0/2.0/3.0, USB Keyboard and SMBIOS 2.5)   |
| <b>Power Configuration</b>  | ACPI/ACPM Power Management<br>Main Switch Override Mechanism<br>Keyboard Wake-up from Soft-Off<br>Internal/External Modem Ring-On<br>Power-on mode for AC power recovery   |
| <b>PC Health Monitoring</b> | <b>CPU Monitoring</b><br>Onboard voltage monitors for CPU core, +3.3V, +5V, +/-12V, +3.3V Stdby, VBAT, Memory, VCORE for CPU<br>CPU 4-phase switching voltage regulator<br>CPU/System overheat LED and control<br>CPU Thermal Trip support<br>Thermal Monitor 2 (TM2) support<br><b>Fan Control</b><br>Fan status monitoring with firmware 4-pin (Pulse Width Modulation) fan speed control<br>Low noise fan speed control |
| <b>System Management</b>    | PECI (Platform Environment Configuration Interface) 2.0 support<br>System resource alert via Supero Doctor III<br>SuperoDoctor III, Watch Dog, NMI<br>Chassis Intrusion header and detection   |
| <b>CD Utilities</b>         | BIOS flash upgrade utility<br>Drivers and software for Intel® C204 chipset utilities   |
| <b>Other</b>                | ROHS 6/6 (Full Compliance, Lead Free)  |
| <b>Dimensions</b>           | ATX form factor (12" x 9.6")   |

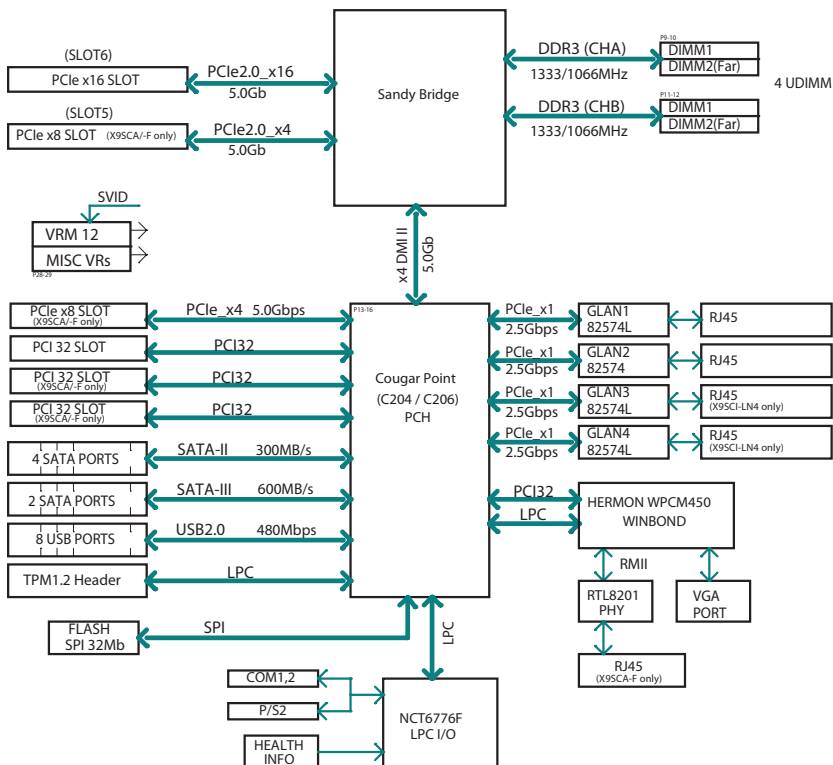


**Note:** For IPMI Configuration Instructions, please refer to the Embedded IPMI Configuration User's Guide available @ <http://www.supermicro.com/support/manuals/>.

X9SCi-LN4/X9SCi-LN4F/X9SCA/X9SCA-F Block Diagram

BLOCK DIAGRAM

RoHS 6/6



System Block Diagram



**Note:** This is a general block diagram and may not exactly represent the features on your motherboard. See the Motherboard Features pages for the actual specifications of each motherboard.

## 1-2 Chipset Overview

The X9SCi-LN4/X9SCA series motherboard supports a single Intel® Xeon E3-1200 series, 2nd generation Intel Core® i3, Pentium®, Celeron® processor in an LGA 1155 socket. Built upon the functionality and the capability of the C204 chipset, the motherboard provides substantial enhancement to system performance and storage capability for Bromolow server platforms in a sleek package.

The high-speed Direct Media Interface (DMI) featured in the Intel C204 chipset supports high-speed Direct Media Interface (DMI) for chip-to-chip true isochronous communication, providing up to 10 Gb/s of software-transparent data transfer rate on each read/write direction. In addition, the X9SCi-LN4/X9SCA series motherboard also features the TCO timer which allows the system to recover from a software/hardware lock and perform tasks, including ECC Error Reporting, Function Disable and Intruder Detect.

### Intel C204 Chipset Features

- Direct Media Interface (up to 10 Gb/s transfer, Full Duplex)
- Intel® Matrix Storage Technology and Intel Rapid Storage Technology
- Intel I/O Virtualization (VT-d) Support
- Intel Trusted Execution Technology Support
- PCI Express 2.0 Interface (up to 5.0 GT/s)
- SATA Controller (up to 3G/s)
- Advanced Host Controller Interface (AHCI)
- Two (2) SATA 3.0 ports (up to 6G/s)

## 1-3 Special Features

### Recovery from AC Power Loss

Basic I/O System (BIOS) provides a setting for you to determine how the system will respond when AC power is lost and then restored to the system. You can choose for the system to remain powered off (in which case you must press the power switch to turn it back on), or for it to automatically return to a power-on state. See the Advanced BIOS Setup section to change this setting. The default setting is **Last State**.

## 1-4 PC Health Monitoring

This section describes the PC health monitoring features of the board. All have an onboard System Hardware Monitoring chip that supports PC health monitoring. An onboard voltage monitor will scan these onboard voltages continuously: CPU Vcore, NIC Vcore, BMC Vcore, AUX Vcore, Standby ME Vcore, 12V Scale, 1.5V, 3.3V Vcc(V), 3.3VSB, and Battery Voltage. Once a voltage becomes unstable, a warning is given, or an error message is sent to the screen. The user can adjust the voltage thresholds to define the sensitivity of the voltage monitor.

### Fan Status Monitor with Firmware Control

PC health monitoring in the BIOS can check the RPM status of the cooling fans. The onboard CPU and chassis fans are controlled by Thermal Management via BIOS (under the Hardware Monitoring section in the Advanced Setting).

### Environmental Temperature Control

The thermal control sensor monitors the CPU temperature in real time and will turn on the thermal control fan whenever the CPU temperature exceeds a user-defined threshold. The overheat circuitry runs independently from the CPU. Once the thermal sensor detects that the CPU temperature is too high, it will automatically turn on the thermal fans to prevent the CPU from overheating. The onboard chassis thermal circuitry can monitor the overall system temperature and alert the user when the chassis temperature is too high.



**Note:** To avoid possible system overheating, please be sure to provide adequate airflow to your system.

### System Resource Alert

This feature is available when the system is used with Supero Doctor III in

the Windows OS environment or used with Supero Doctor II in Linux. Supero Doctor is used to notify the user of certain system events. For example, you can also configure Supero Doctor to provide you with warnings when the system temperature, CPU temperatures, voltages and fan speeds go beyond predefined thresholds.

## 1-5 ACPI Features

ACPI stands for Advanced Configuration and Power Interface. The ACPI specification defines a flexible and abstract hardware interface that provides a standard way to integrate power management features throughout a PC system, including its hardware, operating system and application software. This enables the system to automatically turn on and off peripherals such as CD-ROMs, network cards, hard disk drives and printers.

In addition to enabling operating system-directed power management, ACPI also provides a generic system event mechanism for Plug and Play, and an operating system-independent interface for configuration control. ACPI leverages the Plug and Play BIOS data structures, while providing a processor architecture-independent implementation that is compatible with Windows XP, Windows Vista and Windows 2008 Operating Systems.

### Slow Blinking LED for Suspend-State Indicator

When the CPU goes into a suspend state, the chassis power LED will start to blink to indicate that the CPU is in suspend mode. When the user presses any key, the CPU will "wake up", and the LED will automatically stop blinking and remain on.

## 1-6 Power Supply

As with all computer products, a stable power source is necessary for proper and reliable operation. It is even more important for processors that have high CPU clock rates.

This motherboard accommodates 24-pin ATX power supplies. Although most power supplies generally meet the specifications required by the CPU, some are inadequate. In addition, the 12V 8-pin power connector located at JPW2 is also required to ensure adequate power supply to the system. Also your power supply must supply 1.5A for the Ethernet ports.



**Warning!** 1. To prevent damage to the power supply or motherboard, please use a power supply that contains a 24-pin and a 8-pin power connectors. Be sure to connect these connectors to the 24-pin (JPW1) and the

8-pin (JPW2) power connectors on the motherboard. Failure in doing so will void the manufacturer warranty on your power supply and motherboard.

2. To provide adequate power to SATA devices, please connect the SATA DOM PWR connector (JWF1) to the power supply.

It is strongly recommended that you use a high quality power supply that meets ATX power supply Specification 2.02 or above. It must also be SSI compliant. (For more information, please refer to the web site at <http://www.ssiforum.org/>). Additionally, in areas where noisy power transmission is present, you may choose to install a line filter to shield the computer from noise. It is recommended that you also install a power surge protector to help avoid problems caused by power surges.

## 1-7 Super I/O

The Super I/O supports two high-speed, 16550 compatible serial communication ports (UARTs). Each UART includes a 16-byte send/receive FIFO, a programmable baud rate generator, complete modem control capability and a processor interrupt system. Both UARTs provide legacy speed with baud rate of up to 115.2 Kbps as well as an advanced speed with baud rates of 250 K, 500 K, or 1 Mb/s, which support higher speed modems.

The Super I/O provides functions that comply with ACPI (Advanced Configuration and Power Interface), which includes support of legacy and ACPI power management through an SMI or SCI function pin. It also features auto power management to reduce power consumption.

## 1-8 Overview of the Nuvoton WPCM450 Controller

The Nuvoton WPCM450R Controller is a Baseboard Management Controller (BMC) that supports 2D/VGA-compatible Graphics cores, Virtual Media, and Keyboard/Video/Mouse Redirection (KVMR) modules. With blade-oriented Super I/O capability built in, the WPCM450R Controller is ideal for legacy-reduced server platforms.

The WPCM450R interfaces with a host system via PCI interface to communicate with the Graphics core. It supports USB 2.0 and 1.1 for remote keyboard/mouse/virtual media emulation. It also provides LPC interface to control Super IO functions. The WPCM450R is connected to the network via an external Ethernet PHY module.

The WPCM450R communicates with onboard components via six SMBus interfaces, fan control, and Platform Environment Control Interface (PECI) buses.



**Note:** For more information on IPMI configuration, please refer to the Embedded IPMI User's Guide posted on our Website @ <http://www.supermicro.com/support/manuals/>.

## Chapter 2

### Installation

#### 2-1 Static-Sensitive Devices

Electrostatic-Discharge (ESD) can damage electronic components. To avoid damaging your system board, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

##### **Precautions**

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing the board from the antistatic bag.
- Handle the board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the motherboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure your computer chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the motherboard.
- Use only the correct type of onboard CMOS battery. Do not install the onboard battery upside down to avoid possible explosion.

##### **Unpacking**

The motherboard is shipped in antistatic packaging to avoid static damage. When unpacking the board, make sure that the person handling it is static protected.

## 2-2 Processor and Heatsink Installation



**Warning:** When handling the processor package, avoid placing direct pressure on the label area of the fan.



### Important:

Always connect the power cord last, and always remove it before adding, removing or changing any hardware components. Make sure that you install the processor into the CPU socket before you install the CPU heatsink.

If you buy a CPU separately, make sure that you use an Intel-certified multi-directional heatsink only.

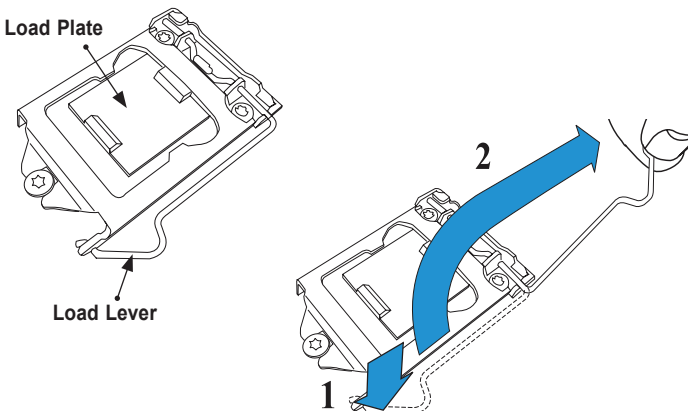
Make sure to install the system board into the chassis before you install the CPU heatsink.

When receiving a server board without a processor pre-installed, make sure that the plastic CPU socket cap is in place and none of the socket pins are bent; otherwise, contact your retailer immediately.

Refer to the Supermicro website for updates on CPU support.

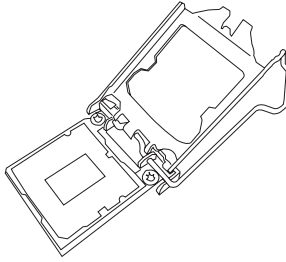
### Installing the LGA1155 Processor

1. Press the load lever to release the load plate, which covers the CPU socket, from its locking position.

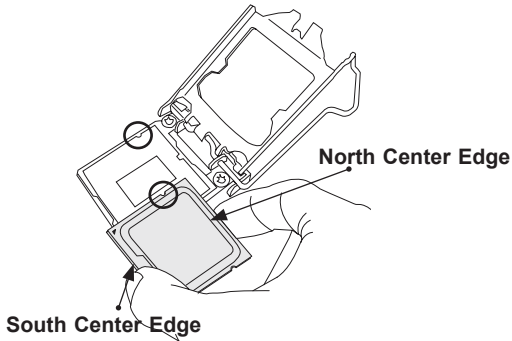




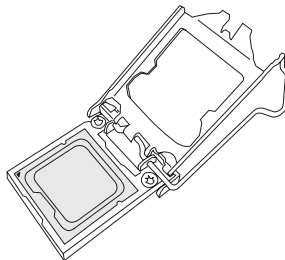
2. Gently lift the load lever to open the load plate. Remove the plastic cap.



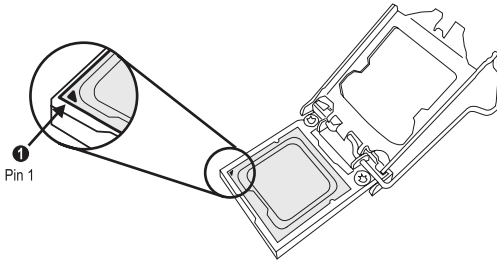
3. Use your thumb and your index finger to hold the CPU at the North center edge and the South center edge of the CPU.



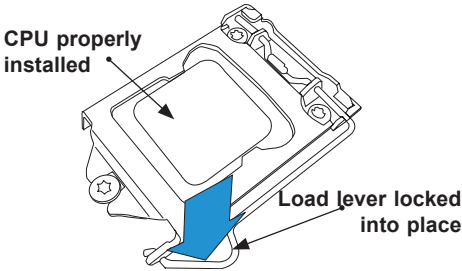
4. Align the CPU key that is the semi-circle cutouts against the socket keys. Once it is aligned, carefully lower the CPU straight down into the socket. (Do not drop the CPU on the socket. Do not move the CPU horizontally or vertically.)



5. Do not rub the CPU against the surface or against any pins of the socket to avoid damaging the CPU or the socket.)



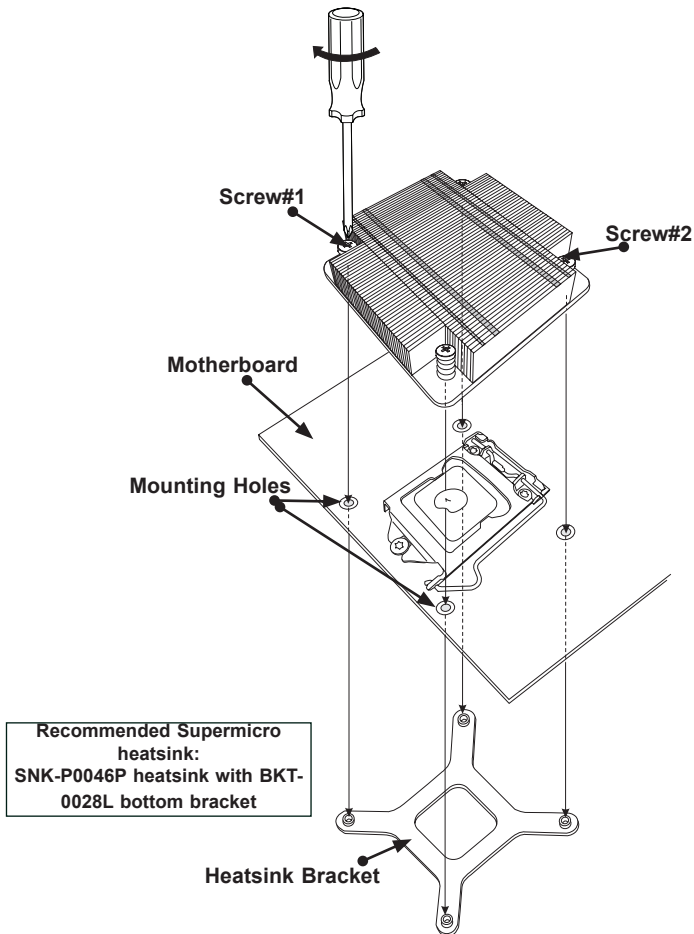
6. With the CPU inside the socket, inspect the four corners of the CPU to make sure that the CPU is properly installed.
7. Use your thumb to gently push the load lever down to the lever lock.



**Warning:** You can only install the CPU inside the socket only in one direction. Make sure that it is properly inserted into the CPU socket before closing the load plate. If it doesn't close properly, do not force it as it may damage your CPU. Instead, open the load plate again and double-check that the CPU is aligned properly.

## Installing a Passive CPU Heatsink

1. Do not apply any thermal grease to the heatsink or the CPU die -- the required amount has already been applied.
2. Place the heatsink on top of the CPU so that the four mounting holes are aligned with those on the Motherboard's and the Heatsink Bracket underneath.
3. Screw in two diagonal screws (i.e., the #1 and the #2 screws) until just snug (-do not over-tighten the screws to avoid possible damage to the CPU.)
4. Finish the installation by fully tightening all four screws.



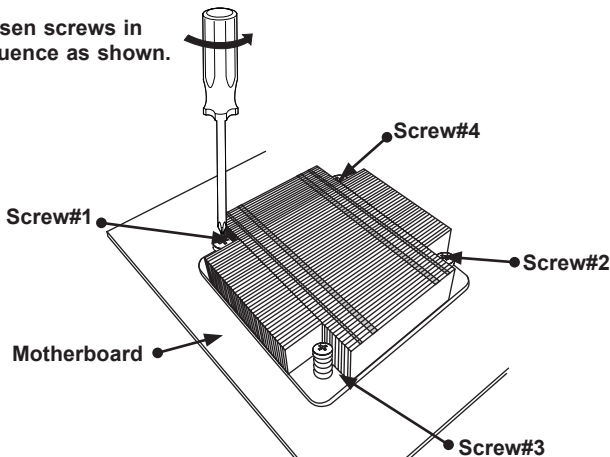
## Removing the Heatsink



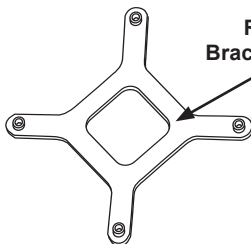
**Warning:** We do not recommend that the CPU or the heatsink be removed. However, if you do need to uninstall the heatsink, please follow the instructions below to uninstall the heatsink to prevent damage done to the CPU or the CPU socket.

1. Unscrew the heatsink screws from the motherboard in the sequence as shown in the illustration below.
2. Gently wriggle the heatsink to loosen it from the CPU. (Do not use excessive force when wriggling the heatsink!!)
3. Once the CPU is loosened, remove the CPU from the CPU socket.
4. Clean the surface of the CPU and the heatsink, removing the used thermal grease. Reapply the proper amount of thermal grease on the surface before re-installing the CPU and the heatsink.

Loosen screws in sequence as shown.



Remove the Heatsink Bracket from underneath the motherboard.



## Installing an Active Fan CPU Heatsink

1. Locate the CPU Fan power connector on the motherboard. (Refer to the layout on the right for the CPU Fan location.)
2. Position the heatsink so that the heatsink fan wires are closest to the CPU fan power connector and are not interfered with other components.
3. Inspect the CPU Fan wires to make sure that the wires are routed through the bottom of the heatsink.
4. Remove the thin layer of the protective film from the copper core of the heatsink.



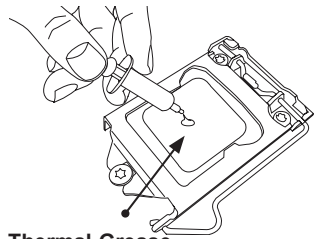
**Warning:** CPU may overheat if the protective film is not removed from the heatsink.

5. Apply the proper amount of thermal grease on the CPU.

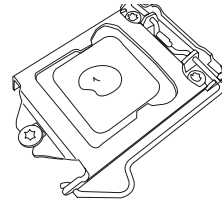


**Note:** if your heatsink came with a thermal pad, please ignore this step.

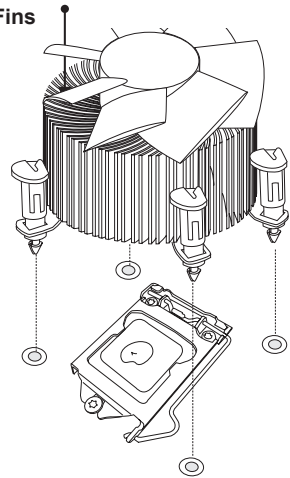
6. If necessary, rearrange the wires to make sure that the wires are not pinched between the heatsink and the CPU. Also make sure to keep clearance between the fan wires and the fins of the heatsink.



**Thermal Grease**

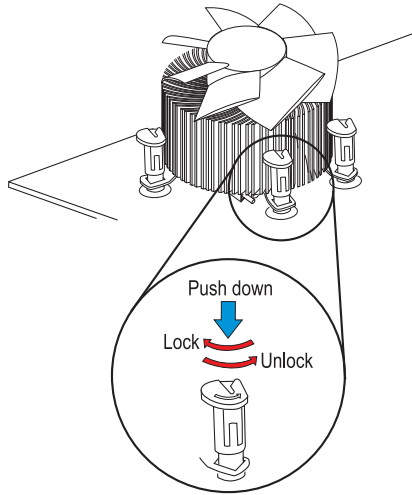


**Heatsink Fins**



**Recommended Supermicro heatsink:  
SNK-P0046A4 active heatsink**

7. Align the four heatsink fasteners with the mounting holes on the motherboard. Gently push the pairs of diagonal fasteners (#1 & #2, and #3 & #4) into the mounting holes until you hear a click. Also, make sure to orient each fastener so that the narrow end of the groove is pointing outward.
8. Repeat Step 7 to insert all four heatsink fasteners into the mounting holes.
9. Once all four fasteners are securely inserted into the mounting holes, and the heatsink is properly installed on the motherboard, connect the heatsink fan wires to the CPU Fan connector.



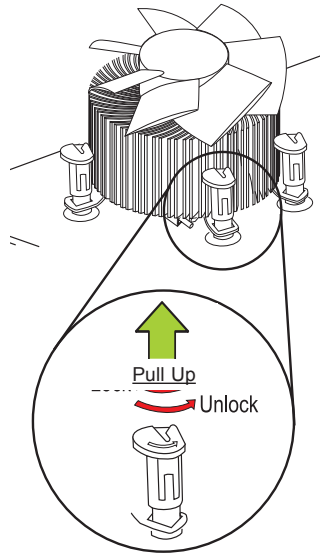
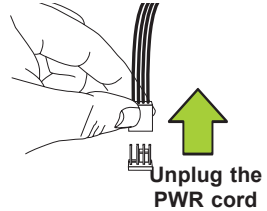
## Removing the Heatsink



**Warning:** We do not recommend that the CPU or the heatsink be removed. However, if you do need to remove the heatsink, please follow the instructions below to remove the heatsink and to prevent damage done to the CPU or other components.

### Active Heatsink Removal

1. Unplug the power cord from the power supply.
2. Disconnect the heatsink fan wires from the CPU fan header.
3. Use your finger tips to gently press on the fastener cap and turn it counterclockwise to make a 1/4 (90°) turn, and pull the fastener upward to loosen it.
4. Repeat Step 3 to loosen all fasteners from the mounting holes.
5. With all fasteners loosened, remove the heatsink from the CPU.



## 2-3 Installing DDR3 Memory



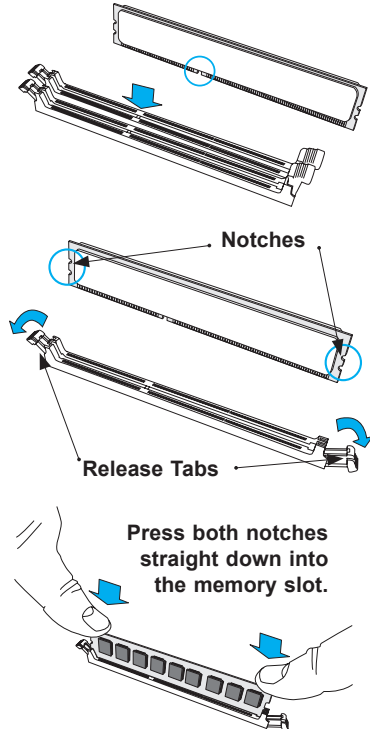
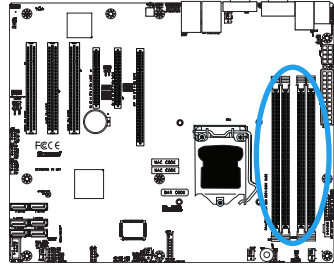
**Note:** Check the Supermicro website for recommended memory modules.

### CAUTION

Exercise extreme care when installing or removing DIMM modules to prevent any possible damage.

### DIMM Installation

1. Insert the desired number of DIMMs into the memory slots, starting with DIMM1A (Slot A, Channel 1, see the next page for the location). For best performance, please use the memory modules of the same type and speed in the same bank.
2. Push the release tabs outwards on both ends of the DIMM slot to unlock it.
3. Align the key of the DIMM module with the receptive point on the memory slot.
4. Align the notches on both ends of the module against the receptive points on the ends of the slot.
5. Use two thumbs together to press the notches on both ends of the module straight down into the slot until the module snaps into place.
6. Press the release tabs to the lock positions to secure the DIMM module into the slot.



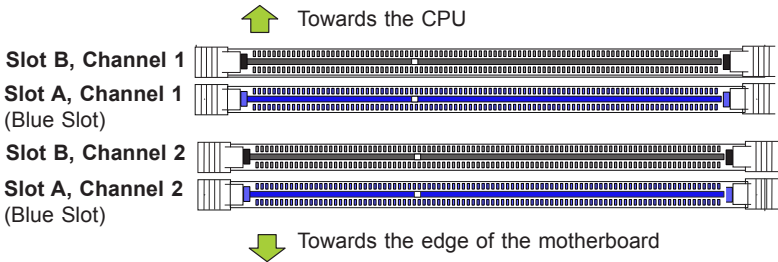
### Removing Memory Modules

Reverse the steps above to remove the DIMM modules from the motherboard.



## Memory Support

The X9SCi-LN4/X9SCA series supports up to 32GB of Unbuffered (UDIMM) DDR3 ECC 1333/1066 MHz in 4 memory slots. Populating these DIMM modules with a pair of memory modules of the same type and same size will result in interleaved memory, which will improve memory performance. Please refer to the table below:



## Memory Population Guidelines

Please follow the table below when populating the X9SCi-LN4/X9SCA series.

| DDR3 Unbuffered ECC (UDIMM) Memory |                             |                 |            |                                  |
|------------------------------------|-----------------------------|-----------------|------------|----------------------------------|
| DIMM Slots per Channel             | DIMMs Populated per Channel | DIMM Type       | POR Speeds | Ranks per DIMM (any combination) |
| 2                                  | 1                           | Unbuffered DDR3 | 1066, 1333 | Single Rank, Dual Rank           |
| 2                                  | 2                           | Unbuffered DDR3 | 1066, 1333 | Single Rank, Dual Rank           |



### Notes

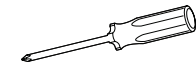
- Due to memory allocation to system devices, the amount of memory that remains available for operational use will be reduced when 4 GB of RAM is used. The reduction in memory availability is disproportional. See the following table for details.
- For Microsoft Windows users: Microsoft implemented a design change in the Windows XP with Service Pack 2 (SP2) and Windows Vista. This change is specific to the behavior of Physical Address Extension (PAE) mode which improves driver compatibility. For more information, please read the following article at Microsoft's Knowledge Base website at: <http://support.microsoft.com/kb/888137>.

| <b>Possible System Memory Allocation &amp; Availability</b>     |             |  |
|---|-------------|--|
| <b>System Device</b>  | <b>Size</b> | <b>Physical Memory Remaining (-Available)<br/>(4 GB Total System Memory)</b> |
| Firmware Hub flash memory (System BIOS)                         | 1 MB        | 3.99   |
| Local APIC  | 4 KB        | 3.99   |
| Area Reserved for the chipset                                   | 2 MB        | 3.99   |
| I/O APIC (4 Kbytes)   | 4 KB        | 3.99   |
| PCI Enumeration Area 1  | 256 MB      | 3.76   |
| PCI Express (256 MB)  | 256 MB      | 3.51   |
| PCI Enumeration Area 2 (if needed) -Aligned on 256-MB boundary- | 512 MB      | 3.01   |
| VGA Memory  | 16 MB       | 2.85   |
| TSEG  | 1 MB        | 2.84   |
| Memory available to OS and other applications                   |             | 2.84   |

## 2-4 Motherboard Installation

All motherboards have standard mounting holes to fit different types of chassis. Make sure that the locations of all the mounting holes for both motherboard and chassis match. Although a chassis may have both plastic and metal mounting fasteners, metal ones are highly recommended because they ground the motherboard to the chassis. Make sure that the metal standoffs click in or are screwed in tightly. Then use a screwdriver to secure the motherboard onto the motherboard tray.

### Tools Needed



Philips Screwdriver

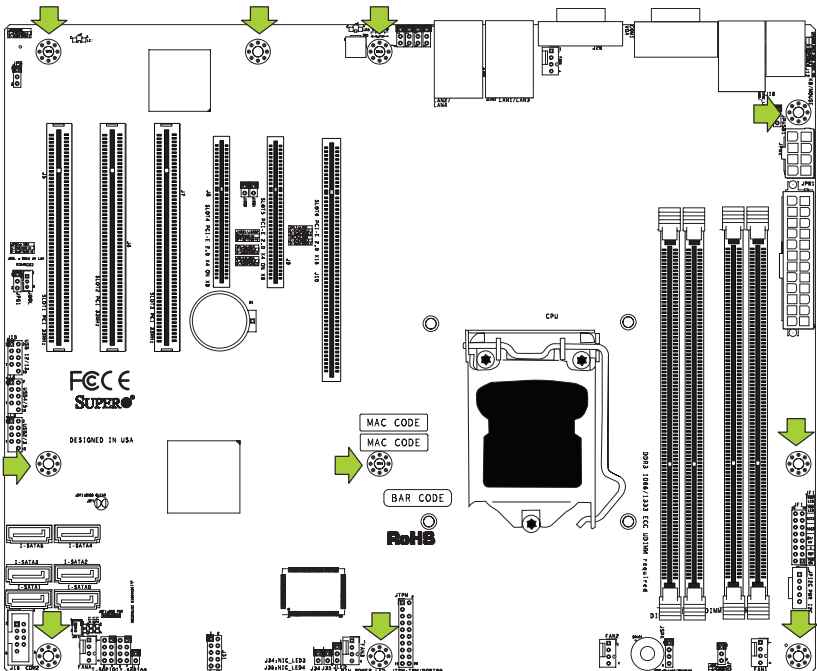


Philips Screws



Standoffs

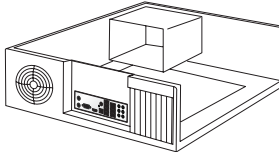
### Location of Mounting Holes



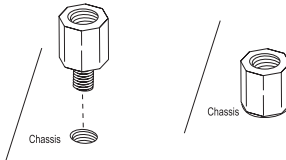
**Caution:** 1) To prevent damage to the motherboard and its components, please do not use a force greater than 8 lb/inch on each mounting screw during motherboard installation. 2) Some components are very close to the mounting holes. Please take precautionary measures to avoid damaging these components when installing the motherboard to the chassis.

## Installing the Motherboard

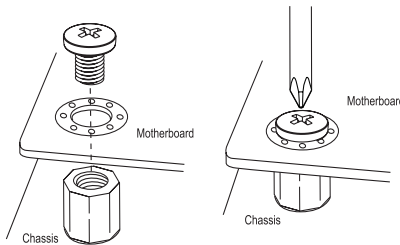
1. Install the I/O shield into the chassis.



2. Locate the mounting holes on the motherboard.
3. Locate the matching mounting holes on the chassis. Align the mounting holes on the motherboard against the mounting holes on the chassis.
4. Install standoffs in the chassis as needed.



5. Install the motherboard into the chassis carefully to avoid damaging motherboard components.
6. Using the Philips screwdriver, insert a Pan head #6 screw into a mounting hole on the motherboard and its matching mounting hole on the chassis.



7. Repeat Step 5 to insert #6 screws into all mounting holes.
8. Make sure that the motherboard is securely placed in the chassis.

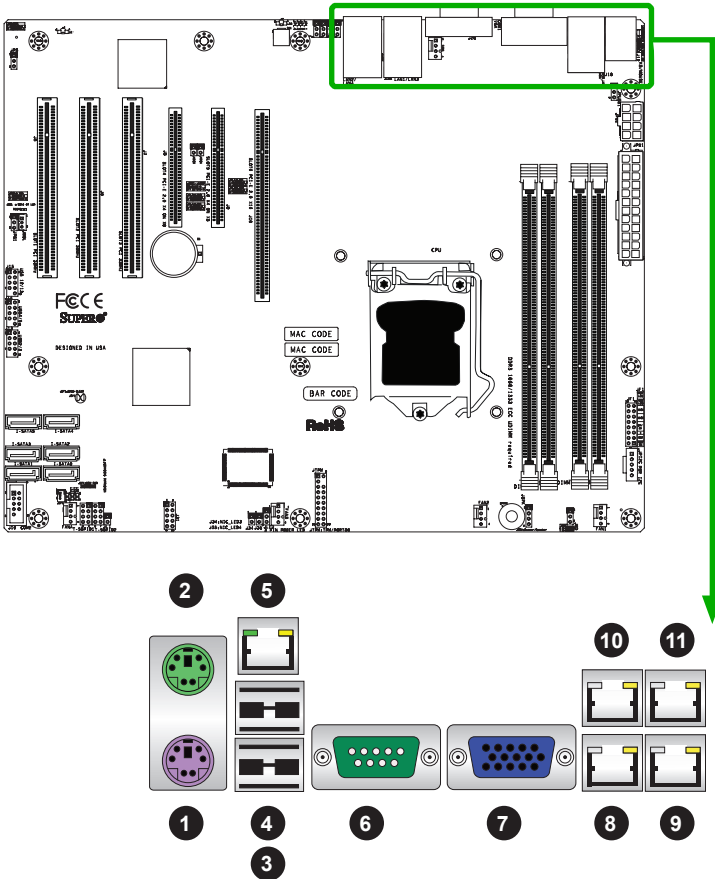


**Note:** Images displayed are is for illustration only. Your chassis or components might look different from those shown in this manual.

## 2-5 Connectors/I/O Ports

The I/O ports are color coded in conformance with the PC 99 specification. See the figure below for the colors and locations of the various I/O ports.

### Motherboard I/O Backpanel



| Motherboard I/O Backpanel             |                                 |
|---------------------------------------|---------------------------------|
| 1. Keyboard (Purple)                  | 6. COM 1                        |
| 2. PS/2 Mouse (Green)                 | 7. VGA                          |
| 3. USB Port 0                         | 8. LAN1                         |
| 4. USB Port 1                         | 9. LAN2                         |
| 5. IPMI LAN (X9SCi-LN4F/X9SCA-F Only) | 10. LAN3 (X9SCi-LN4/-LN4F Only) |
|                                       | 11. LAN4 (X9SCi-LN4/-LN4F Only) |

**ATX PS/2 Keyboard/Mouse Ports**

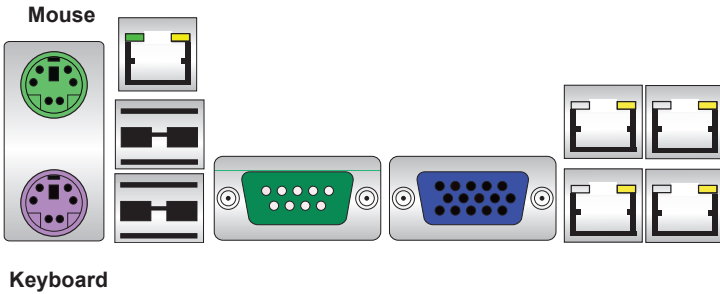
The ATX PS/2 keyboard and PS/2 mouse are located next to the Back Panel USB Ports 0/1 on the motherboard. See the table at right for pin definitions.

| PS/2 Keyboard/Mouse Pin Definitions |                    |           |                    |
|-------------------------------------|--------------------|-----------|--------------------|
| PS2 Keyboard                        |                    | PS2 Mouse |                    |
| Pin#                                | Definition         | Pin#      | Definition         |
| 1                                   | KB Data            | 1         | Mouse Data         |
| 2                                   | No Connection      | 2         | No Connection      |
| 3                                   | Ground             | 3         | Ground             |
| 4                                   | Mouse/KB VCC (+5V) | 4         | Mouse/KB VCC (+5V) |
| 5                                   | KB Clock           | 5         | Mouse Clock        |
| 6                                   | No Connection      | 6         | No Connection      |

VCC: with 1.5A PTC (current limit)

1. Keyboard (Purple)

2. Mouse (Green)



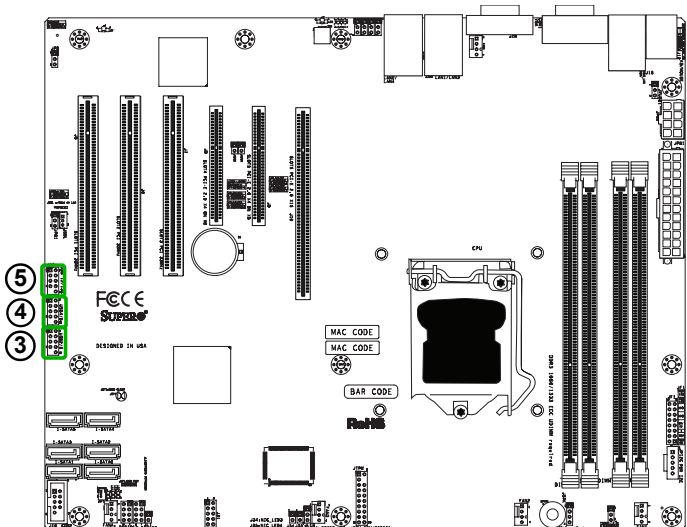
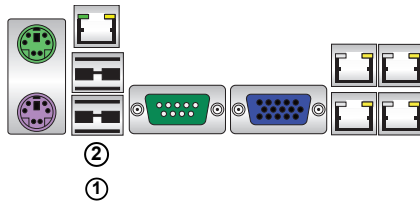
## Universal Serial Bus (USB)

Two Universal Serial Bus ports (USB 0/1) are located on the I/O back panel. In addition, six USB connections (USB 2/3, USB 4/5, USB 12/13) are used to provide front chassis access (USB Cables are not included). See the tables on the right for pin definitions.

1. [Backpanel USB 0](#)
2. [Backpanel USB 1](#)
3. [Front Panel USB 2/3](#)
4. [Front Panel USB 4/5](#)
5. [Front Panel USB 12/13](#)

| Back Panel USB 0/1<br>Pin Definitions |            |      |            |
|---------------------------------------|------------|------|------------|
| Pin#                                  | Definition | Pin# | Definition |
| 1                                     | +5V        | 5    | +5V        |
| 2                                     | USB_PN0    | 6    | USB_PN1    |
| 3                                     | USB_PP0    | 7    | USB_PP1    |
| 4                                     | Ground     | 8    | Ground     |

| Front Panel USB 2/3, 4/5, 12/13<br>Pin Definitions |               |      |            |
|--|---------------|------|------------|
| Pin#   | Definition    | Pin# | Definition |
| 1  | +5V           | 6    | +5V        |
| 2  | USB_PN2       | 7    | USB_PN3    |
| 3  | USB_PP2       | 8    | USB_PP3    |
| 4  | Ground        | 9    | Ground     |
| 5  | No Connection | 10   | Key        |



### Ethernet Ports

Two Ethernet ports (LAN1/LAN2) are located next to the VGA port on the I/O Backpanel. In addition, an IPMI Dedicated LAN is also located above USB 0/1 on the X9SCi-LN4F/X9SCA-F to provide a dedicated network connection for IPMI 2.0. Two additional Ethernet ports (LAN3/LAN4) are also available on the X9SCi-LN4/-LN4F. All these ports accept RJ45 type cables.



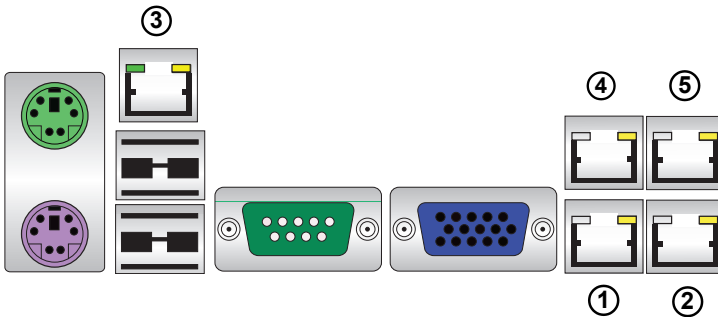
#### Notes

1. The IPMI Dedicated LAN is for the X9SCi-LN4F/X9SCA-F only.
2. Please refer to the LED Indicator Section for LAN LED information.

| LAN Ports<br>Pin Definition |            |    |                                   |
|-----------------------------|------------|----|-----------------------------------|
| Pin#                        | Definition |    |                                   |
| 1                           | TD0-       | 10 | SGND                              |
| 2                           | TD0+       | 11 | P3V3SB                            |
| 3                           | TD1-       | 12 | Act LED                           |
| 4                           | TD1+       | 13 | Link 100 LED<br>(Green, +3V3SB)   |
| 5                           | TD2-       | 14 | Link 1000 LED<br>(Yellow, +3V3SB) |
| 6                           | TD2+       | 15 | Ground                            |
| 7                           | TD3-       | 16 | Ground                            |
| 8                           | TD3+       | 17 | Ground                            |
| 9                           | P2V5SB     | 88 | Ground                            |

(NC: No Connection)

1. LAN1
2. LAN2
3. IPMI Dedicated LAN (X9SCi-LN4F/  
X9SCA-F only)
4. LAN3 (X9SCi-LN4/-LN4F Only)
5. LAN4 (X9SCi-LN4/-LN4F Only)





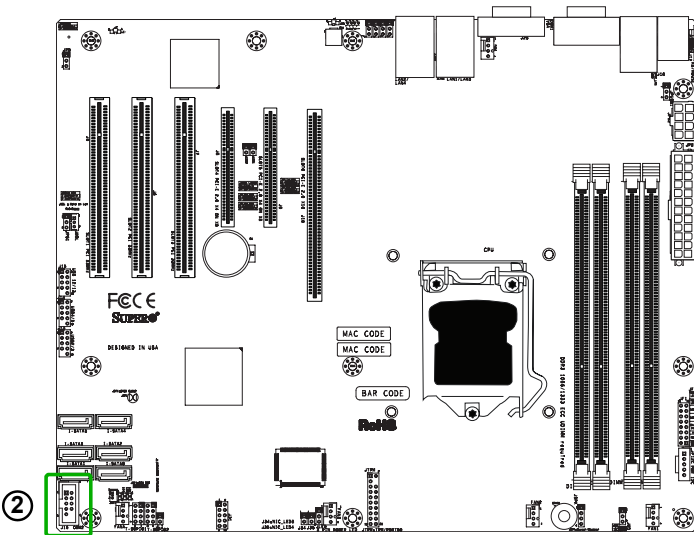
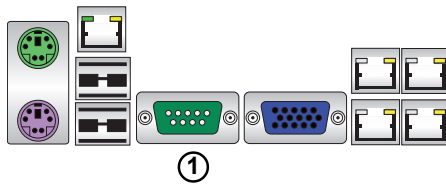
## Serial Ports

A COM Port (COM1) is located on the I/O backpanel, and another Serial Connection (COM2) is located below the SATA ports to provide front access. See the table on the right for pin definitions.

| Serial Port Pin Definitions<br>(COM1/COM2) |            |       |            |
|--|------------|-------|------------|
| Pin #                                      | Definition | Pin # | Definition |
| 1  | DCD        | 6     | DSR        |
| 2  | RXD        | 7     | RTS        |
| 3  | TXD        | 8     | CTS        |
| 4  | DTR        | 9     | RI         |
| 5  | Ground     | 10    | NC         |

1. COM1

2. COM2



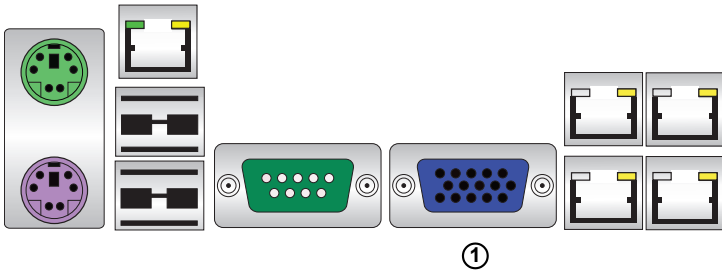
**Video Connector**

A Video (VGA) connector is located next to the COM Port on the I/O backpanel. This connector is used to provide a connection for video and graphics display. Refer to the board layout below for the location.

| VGA Pin Definitions |            |      |                     |
|---------------------|------------|------|---------------------|
| Pin#                | Definition | Pin# | Definition          |
| 1                   | Red        | 10   | Ground              |
| 2                   | Green      | 11   | NC                  |
| 3                   | Blue       | 12   | MS1: SDA (DDC Data) |
| 4                   | NC         | 13   | HSYNC               |
| 5                   | Ground     | 14   | VSYSNC              |
| 6                   | Ground     | 15   | MS3: SCL (DDC CLK)  |
| 7                   | Ground     | 16   | Case                |
| 8                   | Ground     | 17   | Case                |
| 9                   | 5V         |      |                     |

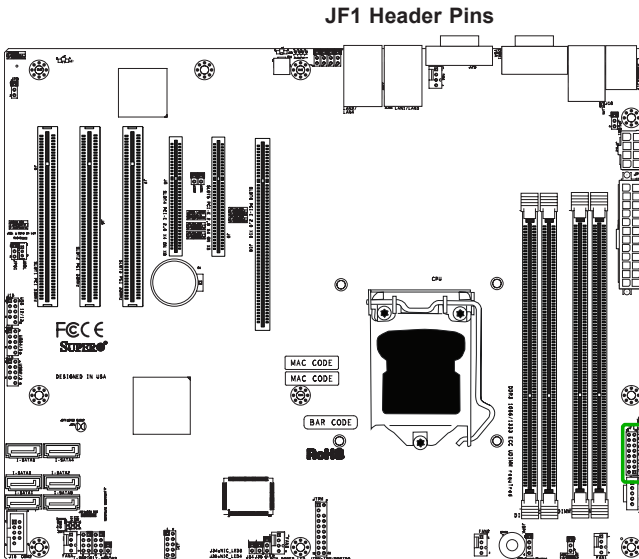
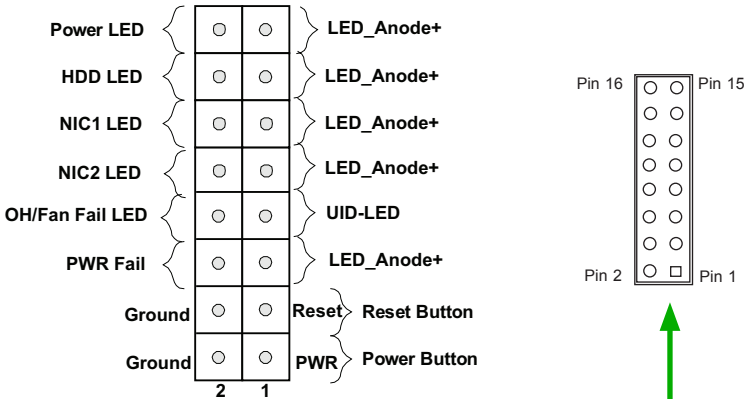
NC= No Connection

1. VGA



## Front Control Panel

JF1 contains header pins for various buttons and indicators that are normally located on a control panel at the front of the chassis. These connectors are designed specifically for use with Supermicro server chassis. See the figure below for the descriptions of the various control panel buttons and LED indicators. Refer to the following section for descriptions and pin definitions.



## Front Control Panel Pin Definitions

### Power LED

The Power LED connection is located on pins 15 and 16 of JF1. Refer to the table on the right for pin definitions.

| Power LED Pin Definitions (JF1) |            |
|---------------------------------|------------|
| Pin#                            | Definition |
| 15                              | +5V        |
| 16                              | Ground     |

### HDD LED

The HDD LED connection is located on pins 13 and 14 of JF1. Attach a cable here to indicate the status of HDD-related activities, including IDE, SATA activities. See the table on the right for pin definitions.

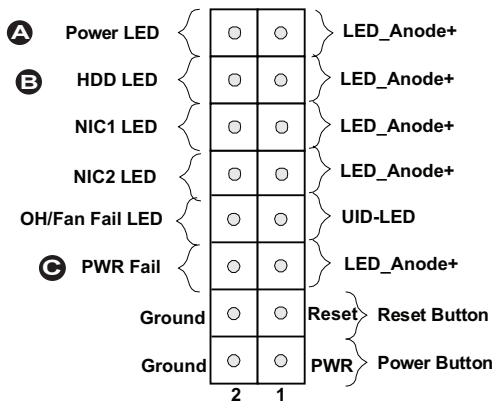
| HDD LED Pin Definitions (JF1) |            |
|-------------------------------|------------|
| Pin#                          | Definition |
| 13                            | +5V        |
| 14                            | HD Active  |

### Power Fail LED

The Power Fail LED connection is located on pins 5 and 6 of JF1. Refer to the table on the right for pin definitions.

| PWR Fail LED Pin Definitions (JF1) |            |
|------------------------------------|------------|
| Pin#                               | Definition |
| 5                                  | Vcc        |
| 6                                  | Ground     |

- A. PWR LED
- B. HDD LED
- C. PWR Fail LED



**NIC1/NIC2 (LAN1/LAN2)**

The NIC (Network Interface Controller) LED connection for LAN port 1 is located on pins 11 and 12 of JF1, and the LED connection for LAN Port 2 is on Pins 9 and 10. NIC1 LED and NIC2 LED are 2-pin NIC LED headers. Attach NIC LED cables to NIC1 and NIC2 LED indicators to display network activities. Refer to the table on the right for pin definitions.

| LAN1/LAN2 LED<br>Pin Definitions (JF1) |            |
|--|------------|
| Pin#                                   | Definition |
| 9/11                                   | Vcc        |
| 10/12                                  | Ground     |

**Overheat (OH)/Fan Fail / UID LED**

Connect an LED cable to the OH/Fan Fail connections on pins 7 and 8 of JF1 to display signals or to provide advanced warnings for chassis overheat/fan failure. Refer to the table on the right for pin definitions. Refer to page 2-31 for information on UID-LED.

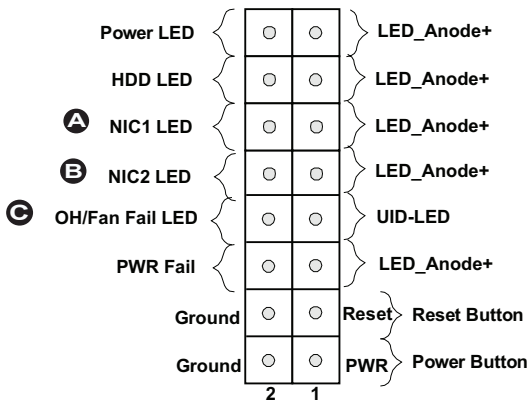
| OH/Fan Fail LED<br>Pin Definitions (JF1) |                  |
|--|------------------|
| Pin#                                     | Definition       |
| 7  | Vcc/Blue UID LED |
| 8  | OH/Fan Fail LED  |

| OH/Fan Fail Indicator<br>Status |            |
|---------------------------------|------------|
| State                           | Definition |
| Off                             | Normal     |
| On                              | Overheat   |
| Flash-<br>ing                   | Fan Fail   |

A. NIC1 LED

B. NIC2 LED

C. OH/Fan Fail / UID LED



### Reset Button

The Reset Button connection is located on pins 3 and 4 of JF1. Attach it to a hardware reset switch on the computer case to reset the system. Refer to the table on the right for pin definitions.

| Reset Button<br>Pin Definitions (JF1) |            |
|---------------------------------------|------------|
| Pin#                                  | Definition |
| 3                                     | Reset      |
| 4                                     | Ground     |

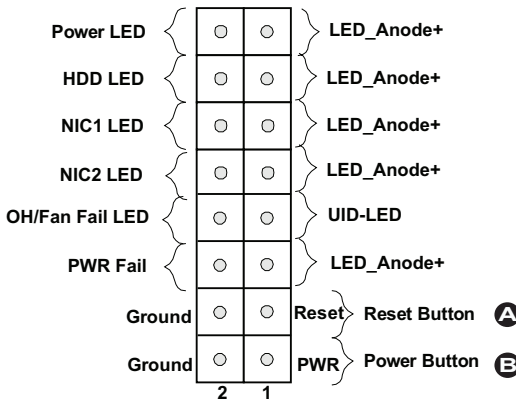
### Power Button

The Power Button connection is located on pins 1 and 2 of JF1. Momentarily contacting both pins will power on/off the system. This button can also be configured to function as a suspend button (with a setting in the BIOS - see Chapter 4). To turn off the power in the suspend mode, press the button for at least 4 seconds. Refer to the table on the right for pin definitions.

| Power Button<br>Pin Definitions (JF1) |             |
|---------------------------------------|-------------|
| Pin#                                  | Definition  |
| 1                                     | Signal      |
| 2                                     | +3V Standby |

A. Reset Button

B. PWR Button

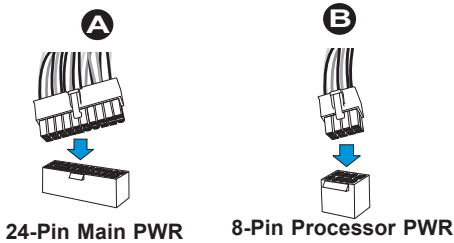


## 2-6 Connecting Cables

This section provides brief descriptions and pin-out definitions for onboard headers and connectors. Be sure to use the correct cable for each header or connector.

### ATX Main PWR & CPU PWR Connectors

The 24-pin main power connector (JPW1) is used to provide power to the motherboard. The 8-pin CPU PWR connector (JPW2) is also required for the processor. These power connectors meet the SSI EPS 12V specification. See the table on the right for pin definitions.



A. 24-Pin ATX Main PWR

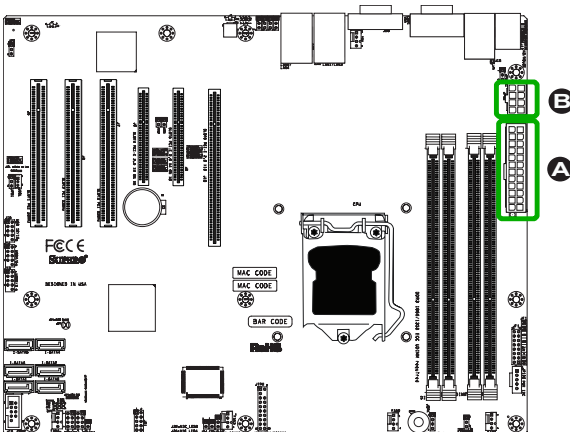
B. 8-Pin Processor PWR

| Pin# | Definition | Pin # | Definition |
|------|------------|-------|------------|
| 13   | +3.3V      | 1     | +3.3V      |
| 14   | -12V       | 2     | +3.3V      |
| 15   | COM        | 3     | COM        |
| 16   | PS_ON      | 4     | +5V        |
| 17   | COM        | 5     | COM        |
| 18   | COM        | 6     | +5V        |
| 19   | COM        | 7     | COM        |
| 20   | Res (NC)   | 8     | PWR_OK     |
| 21   | +5V        | 9     | 5VSB       |
| 22   | +5V        | 10    | +12V       |
| 23   | +5V        | 11    | +12V       |
| 24   | COM        | 12    | +3.3V      |

12V 8-pin Power Connector Pin Definitions

| Pins        | Definition |
|-------------|------------|
| 1 through 4 | Ground     |
| 5 through 8 | +12V       |

(Required)



### Fan Headers (FAN1~4, FANA)

The X9SCi-LN4/X9SCA series has five fan headers (Fan 1~Fan 4 and Fan A). These fans are 4-pin fan headers. However, Pins 1-3 of the fan headers are backward compatible with the traditional 3-pin fans. A fan speed control setting in the BIOS Hardware Monitoring section allows the BIOS to automatically set fan speeds based on the system temperature. Refer to the table on the right for pin definitions.

| Fan Header Pin Definitions |                |
|----------------------------|----------------|
| Pin#                       | Definition     |
| 1                          | Ground (Black) |
| 2                          | +12V (Red)     |
| 3                          | Tachometer     |
| 4                          | PWM_Control    |

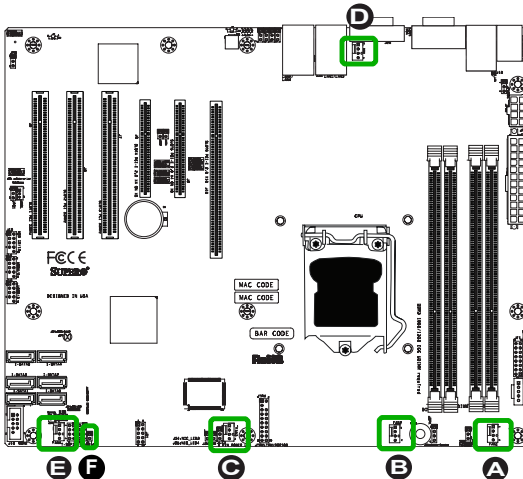


**Note:** Please use all 3-pin fans or all 4-pin fans on a motherboard. Please do not use 3-pin fans and 4-pin fans on the same board.

### Chassis Intrusion (JL1)

A Chassis Intrusion header is located at JL1 on the motherboard. Attach the appropriate cable from the chassis to inform you of a chassis intrusion when the chassis is opened.

| Chassis Intrusion Pin Definitions (JL1) |                 |
|---|-----------------|
| Pin#                                    | Definition      |
| 1                                       | Intrusion Input |
| 2                                       | Ground          |



- A. Fan 1
- B. Fan 2
- C. Fan 3
- D. Fan 4
- E. Fan A
- F. Chassis Intrusion



### Internal Buzzer (SPKR1)

The Internal Buzzer (SPKR1) can be used to provide audible indications for various beep codes. See the table on the right for pin definitions.

| Internal Buzzer<br>Pin Definition |          |               |
|-----------------------------------|----------|---------------|
| Pin#                              |          | Definitions   |
| Pin 1                             | Pos. (+) | Beep In       |
| Pin 2                             | Neg. (-) | Alarm Speaker |

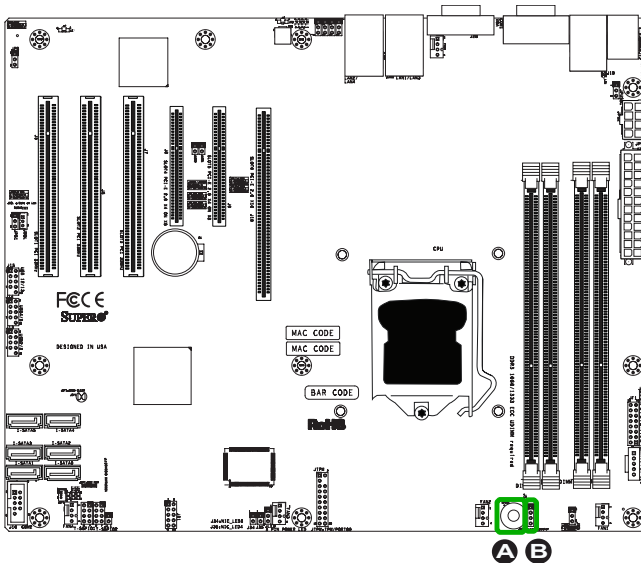
### Speaker (JSPK)

On the JSPK header, Pins 3~4 are used for internal speaker. Close Pins 3~4 with a cap to use the onboard speaker. If you wish to use an external speaker, close Pins 1~4 with a cable. See the table on the right for pin definitions.

| Speaker Connector<br>Pin Definitions |                  |
|--------------------------------------|------------------|
| Pin Setting                          | Definition       |
| Pins 3~4                             | Internal Speaker |
| Pins1~4                              | External Speaker |

A. Internal Buzzer

B. Speaker Header



**Onboard Power LED (JLED1)**

An onboard Power LED header is located at JLED1. This Power LED header is connected to Front Control Panel located at JF1 to indicate the status of system power. See the table on the right for pin definitions.

| Onboard PWR LED Pin Definitions |                              |
|---------------------------------|------------------------------|
| Pin#                            | Definition                   |
| 1                               | VCC                          |
| 2                               | No Connection                |
| 3                               | Connection to PWR LED in JF1 |

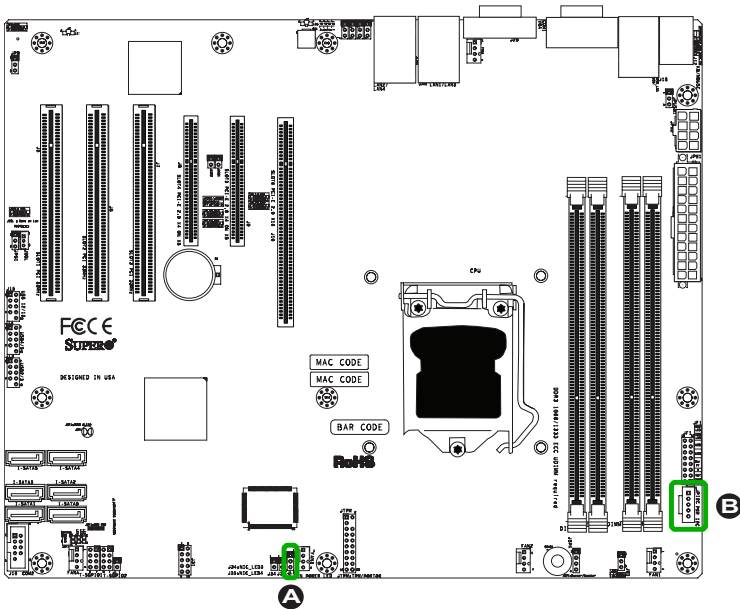
**Power Supply I<sup>2</sup>C (JPI<sup>2</sup>C)**

Power Supply I<sup>2</sup>C Connector, located at JPI<sup>2</sup>C, monitors the status of the power supply, fan and system temperature. See the table on the right for pin definitions.

| PWR Supply I <sup>2</sup> C Pin Definitions |            |
|---|------------|
| Pin#  | Definition |
| 1   | Clock      |
| 2   | Data       |
| 3   | PWR Fail   |
| 4   | Ground     |
| 5   | 3.3V       |

A. PWR LED

B. PWR SMB



### T-SGPIO 0/1 Headers (T-SGPIO)

Two T-SGPIO (Serial-Link General Purpose Input/Output) headers are located near the SATA connectors on the motherboard. These headers are used to communicate with the enclosure management chip in the system. See the table on the right for pin definitions. Refer to the board layout below for the locations of the headers.

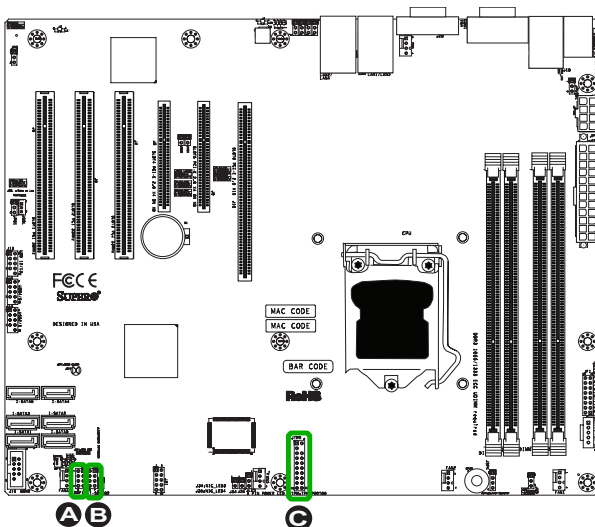
### TPM Header (JTPM)

This header is used to connect a Trusted Platform Module (TPM), which is available from a third-party vendor. A TPM is a security device that supports encryption and authentication in hard drives. It enables the motherboard to deny access if the TPM associated with the hard drive is not installed in the system. See the table on the right for pin definitions.

| Serial_Link-SGPIO<br>Pin Definitions |            |     |            |
|--------------------------------------|------------|-----|------------|
| Pin#                                 | Definition | Pin | Definition |
| 1                                    | NC         | 2   | NC         |
| 3                                    | Ground     | 4   | DATA Out   |
| 5                                    | Load       | 6   | Ground     |
| 7                                    | Clock      | 8   | NC         |

NC: No Connections

| Trusted Platform Module Header<br>Pin Definitions |            |       |            |
|---|------------|-------|------------|
| Pin #   | Definition | Pin # | Definition |
| 1   | LCLK       | 2     | GND        |
| 3   | LFRAME     | 4     | No Pin     |
| 5   | LRESET     | 6     | VCC5       |
| 7   | LAD3       | 8     | LAD2       |
| 9   | VCC3       | 10    | LAD1       |
| 11  | LAD0       | 12    | GND        |
| 13  | RSV0       | 14    | RSV1       |
| 15  | SB3V       | 16    | SERIRQ     |
| 17  | GND        | 18    | CLKRUN     |
| 19  | LPCPD      | 20    | RSV2       |



A. T-SGPIO 0

B. T-SGPIO 1

C. TPM Header

**DOM PWR Connector (JWF1)**

The Disk-On-Module (DOM) power connector, located at JWF1, provides 5V (Gen1/Gen) power to a solid\_state DOM storage device connected to one of the SATA ports. See the table on the right for pin definitions.

| DOM PWR<br>Pin Definitions |            |
|----------------------------|------------|
| Pin#                       | Definition |
| 1                          | 5V         |
| 2                          | Ground     |
| 3                          | Ground     |

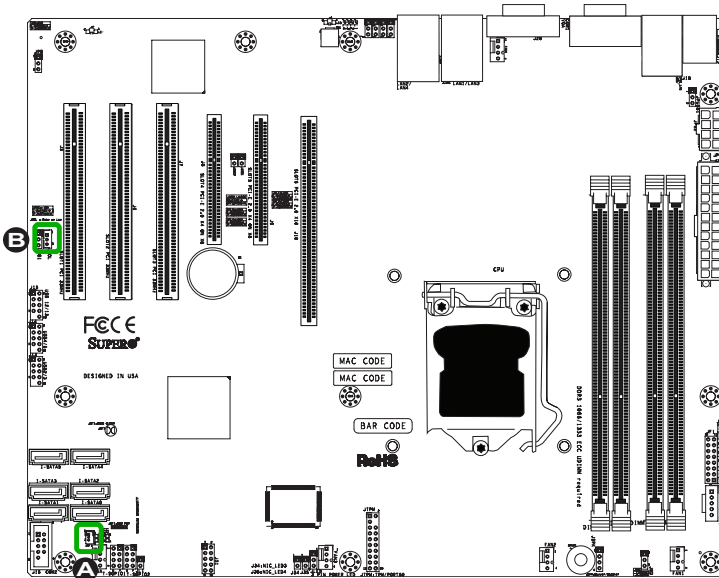
**Wake-On-LAN (JWOL)**

The Wake-On-LAN header is located at JWOL on the motherboard. See the table on the right for pin definitions. (You must also have a LAN card with a Wake-On-LAN connector and cable to use this feature.)

| Wake-On-LAN<br>Pin Definitions<br>(JWOL) |             |
|--|-------------|
| Pin#                                     | Definition  |
| 1  | +5V Standby |
| 2  | Ground      |
| 3  | Wake-up     |

A. DOM PWR

B. JWOL



### NIC3/NIC4 (NIC LED3/LED4)

Attach LED cables to the NIC LED3 and NIC LED4 headers to display network activity for LAN3 and LAN4 ports respectively. Refer to the table on the right for pin definitions. Note: Available only on the X9SCi-LN4/-LN4F.

| LAN3/LAN4 LED<br>Pin Definitions (JF1) |            |
|--|------------|
| Pin#                                   | Definition |
| 1                                      | Vcc        |
| 2                                      | Act LED -  |

### Unit ID Switch

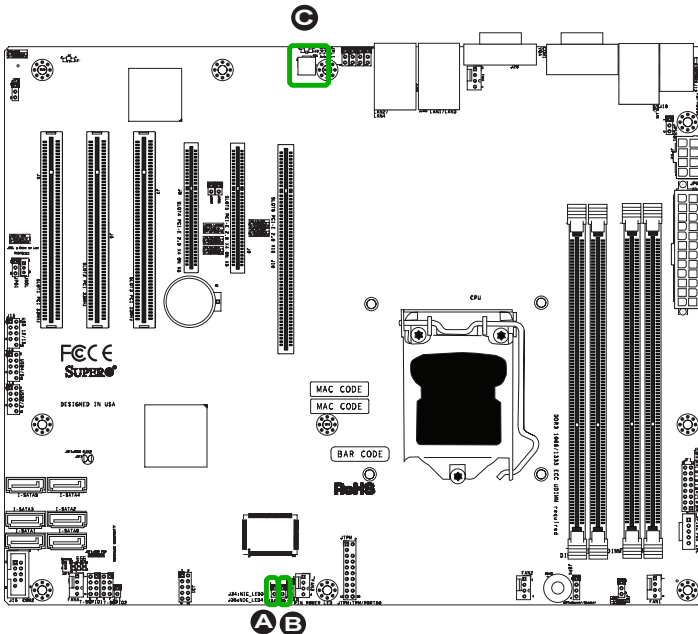
The rear UID switch, the rear UID LED and front UID LED on JF1 are designed to work together. When the user pushes the rear UID switch, the blue backpanel UID LED (LE5) and front UID LED will turn on. Push the rear UID switch again to turn off UID LED (LE5) and the front UID LED. This provides easy identification of a system unit that may be in need of service.

| UID Switch |            |
|------------|------------|
| Pin#       | Definition |
| 1          | Ground     |
| 2          | Ground     |
| 3          | Button In  |
| 4          | Ground     |

A. NIC LED3

B. NIC LED4

C. UID Switch



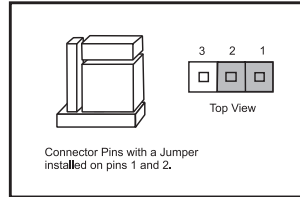
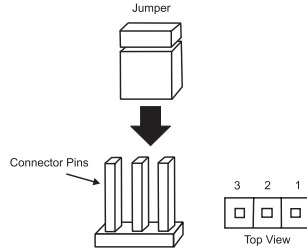
## 2-7 Jumper Settings

### Explanation of Jumpers

To modify the operation of the motherboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board.



**Note:** On two pin jumpers, "Closed" means the jumper is on, and "Open" means the jumper is off the pins.

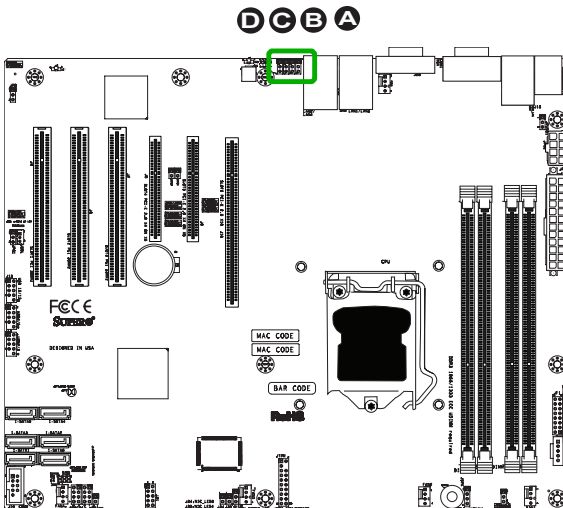


### LAN Port Enable/Disable (JPL1~4)


Jumpers JPL1/JPL2 enable or disable LAN Port 1/LAN Port 2 on the motherboard. See the table on the right for jumper settings. The default setting is enabled. JPL3/JPL4 are available only on the X9SCi-LN4/-LN4F.

| GLAN Enable Jumper Settings |                   |
|-----------------------------|-------------------|
| Pin#                        | Definition        |
| 1-2                         | Enabled (default) |
| 2-3                         | Disabled          |

- A. LAN Port 1 Enable
- B. LAN Port 2 Enable
- C. LAN Port 3 Enable
- D. LAN Port 4 Enable



## CMOS Clear (JBT1)

JBT1 is used to clear CMOS. Instead of pins, this "jumper" consists of contact pads to prevent accidental clearing of CMOS. To clear CMOS, use a metal object such as a small screwdriver to touch both pads at the same time to short the connection. Always remove the AC power cord from the system before clearing CMOS. 



**Note:** For an ATX power supply, you must completely shut down the system, remove the AC power cord and then short JBT1 to clear CMOS.

## PCI Slot SMB Enable (J<sup>2</sup>C1/J<sup>2</sup>C2)

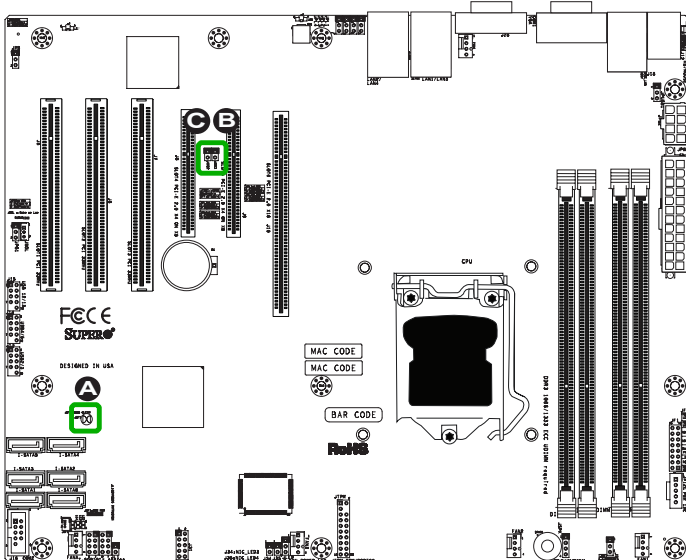
Use Jumpers J<sup>2</sup>C1/J<sup>2</sup>C2 to enable PCI SMB (System Management Bus) support to improve system management for the PCI slots. See the table on the right for jumper settings.

| PCI Slot_SMB Enable<br>Jumper Settings |            |
|--|------------|
| Jumper Setting                         | Definition |
| Short                                  | Enabled    |
| Open (Default)                         | Disabled   |

A. Clear CMOS

B. J<sup>2</sup>C1

C. J<sup>2</sup>C2



**VGA Enable (JPG1)**

JPG1 allows you to enable or disable the onboard VGA connector. The default position is on pins 1 and 2 to enable VGA. See the table on the right for jumper settings.

| VGA Enable/Disable Jumper Settings (JPG1) |            |
|---|------------|
| Both Jumpers                              | Definition |
| Pins 1-2                                  | Enabled    |
| Pins 2-3                                  | Disabled   |

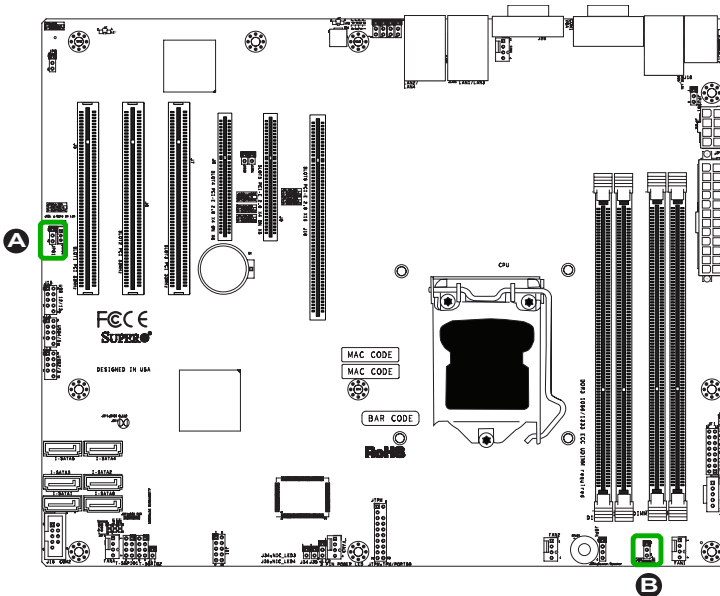
**Watch Dog Enable (JWD)**

Watch Dog (JWD) is a system monitor that can reboot the system when a software application hangs. Close Pins 1-2 to reset the system if an application hangs. Close Pins 2-3 to generate a non-maskable interrupt signal for the application that hangs. See the table on the right for jumper settings. Watch Dog must also be enabled in the BIOS.

| Watch Dog Jumper Settings |                 |
|---------------------------|-----------------|
| Jumper Setting            | Definition      |
| Pins 1-2                  | Reset (default) |
| Pins 2-3                  | NMI             |
| Open                      | Disabled        |

A. VGA Enable

B. Watch Dog Timer Enable





## USB Wake-Up (JPUSB1)

Use the jumper JPUSB1 to "wake-up" your system by pressing a key on a USB keyboard or clicking the USB mouse connected to the Backplane USB Ports 0/1. JPUSB1 is used together with a USB Wake-Up feature in the BIOS. Enable this jumper and the USB support in the BIOS to wake up your system via USB devices.



**Note:** When the USB is set to Enabled in the BIOS, and JPUSB1 is set to Disabled, remove the USB devices from Backplane USB Ports 0/1 before the system goes into the standby mode.

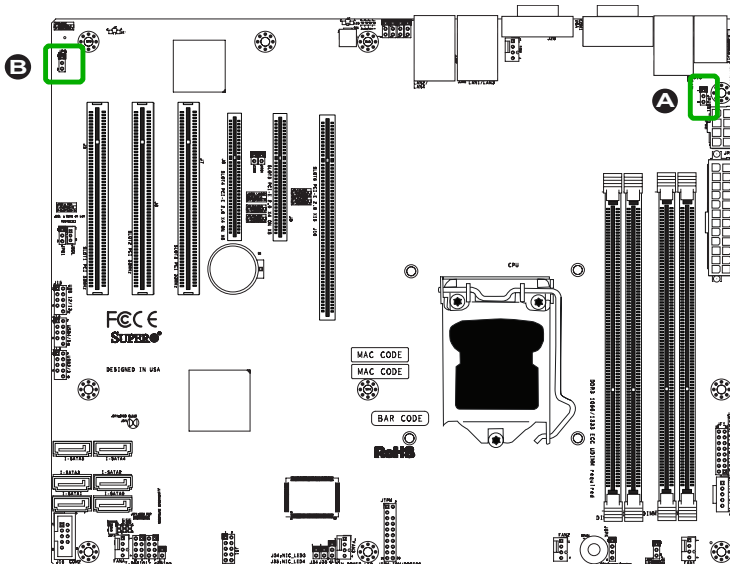
| JPUSB1 (Backplane USB 0/1 Wake-up Enable) |                   |
|---|-------------------|
| Pin#                                      | Definition        |
| 1-2                                       | Enabled (Default) |
| 2-3                                       | Disabled          |

## BMC Enable (JPB)

JPB is used to enable or disable the BMC (Baseboard Management Control) chip and the onboard IPMI connection. This jumper is used together with the IPMI settings in the BIOS. See the table on the right for jumper settings.

| BMC IPMI Enable/Disable Jumper Settings |                   |
|---|-------------------|
| Settings                                | Definition        |
| Pins 1-2                                | Enabled (Default) |
| Pins 2-3                                | Disabled          |

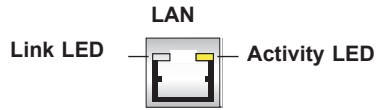
- A. USB 0/1 Wake-up  
B. BMC Enable



## 2-8 Onboard Indicators

### LAN Port LEDs

The LAN ports are located on the I/O backpanel of the motherboard. Each Ethernet LAN port has two LEDs. The yellow LED indicates activity, while the Link LED may be green, amber, or off to indicate the speed of the connections. See the tables at right for more information.

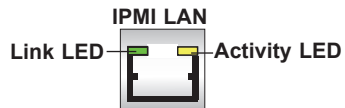


| LAN Link LEDs (Green/Amber/Off) |                          |
|---------------------------------|--------------------------|
| LED Color                       | Definition               |
| Off                             | No Connection or 10 Mbps |
| Green                           | 100 Mbps                 |
| Amber                           | 1 Gbps                   |

### IPMI Dedicated LAN LEDs

In addition to the LAN ports, an IPMI Dedicated LAN is also located on the X9SCi-LN4F/X9SCA-F. The yellow LED on the right indicates activity, while the green LED on the left indicates the speed of the connection. See the tables at right for more information.

| IPMI LAN Link LED (Left) & Activity LED (Right) |                  |            |
|---|------------------|------------|
| Status  | Color            | Definition |
| Link (Left)                                     | Green: Solid     | 100 Mbps   |
| Activity (Right)                                | Yellow: Blinking | Active     |



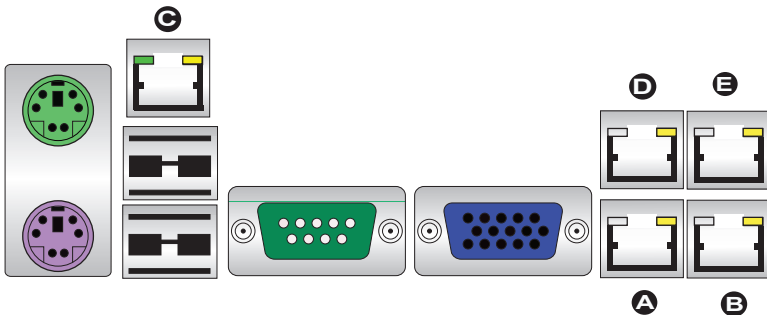
A. LAN1

B. LAN2

C. IPMI Dedicated LAN (X9SCi-LN4F/X9SCA-F only)

D. LAN3 (X9SCi-LN4/-LN4F Only)

E. LAN4 (X9SCi-LN4/-LN4F Only)



Motherboard I/O Backpanel

### Onboard Power LED (LE2)

An Onboard Power LED is located at LE2 on the motherboard. When LE2 is on, the AC power cable is connected and the system is running.

| Onboard PWR LED (LE2)<br>LED Status |                          |
|-------------------------------------|--------------------------|
| Status                              | Definition               |
| Off                                 | System Off (Soft Switch) |
| On                                  | System is Running        |

### Unsupported Memory LED (LE3)

LE3 is located next to LE2. When LE3 blinks, it is an indication/warning that unsupported memory is installed in one (or more) of the memory slots.

| Unsupported Memory LED Indicator<br>(LE3) LED Settings |                                 |
|--|---------------------------------|
| Yellow: Blinking                                       | Unsupported Memory is Installed |

### Onboard Standby Power LED (LE4)

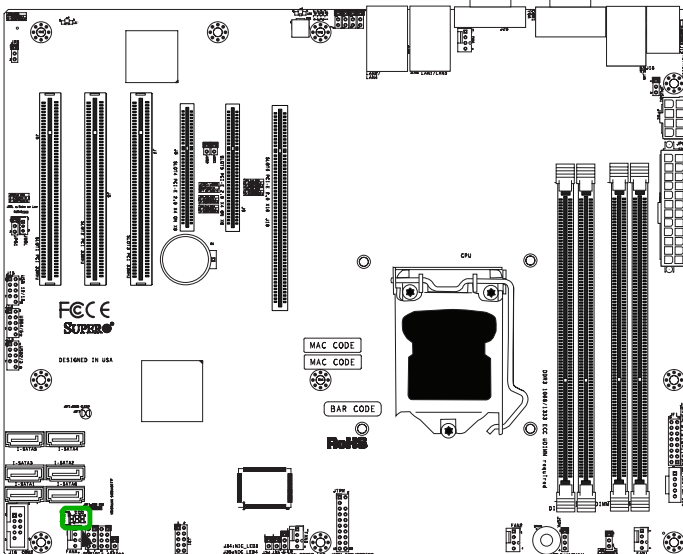
An onboard Standby Power LED is located at LE4. When LE4 is on, the AC power cable is connected and the power supply hard switch is on. Make sure to disconnect the power cable before removing or installing any component. See the layout below for the LED location.

| Onboard Standby PWR LED (LE4)<br>LED Status |                                   |
|---|-----------------------------------|
| Status                                      | Definition                        |
| Off   | Power Supply is Off (Hard Switch) |
| On  | Power Supply is On (Hard Switch). |

A. LE2

B. LE3

C. LE4



**CBA**

**Rear UID LED (LE5)**

The rear UID LED is located at LE5 on the backpanel. This LED is used in conjunction with the rear UID switch to provide easy identification of a system that might be in need of service.

| Rear UID LED (LE5)<br>LED Settings |                |
|------------------------------------|----------------|
| Blue: Solid                        | UID Toggled On |

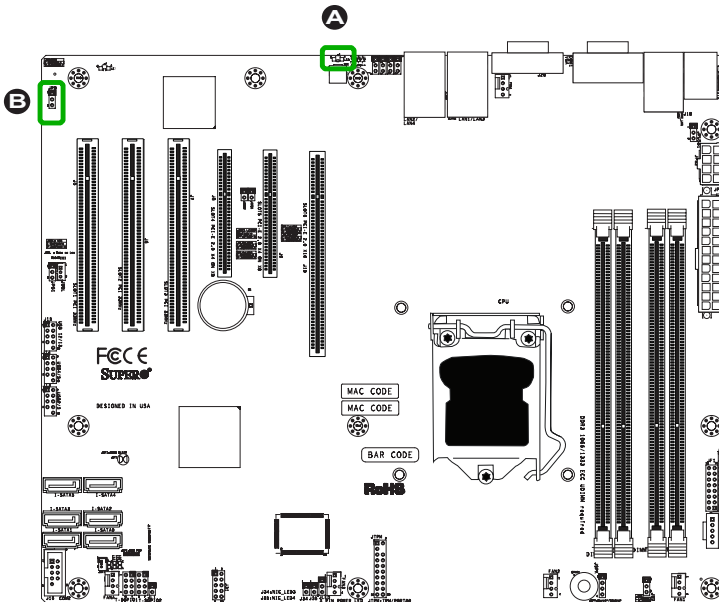
**IPMI Heartbeat LED (LE7)**

An IPMI Heartbeat LED is located at LE7. When LE7 blinks, the IPMI is functioning properly. Refer to the table on the right for details. Also see the layout below for the LED location.

| IPMI Heartbeat LED Indicator (LE7)<br>LED Settings |                       |
|--|-----------------------|
| Green: Blinking                                    | IPMI is ready for use |

A. UID LED

B. IPMI Heartbeat



## 2-9 SATA Connections

### SATA Connections

Six Serial ATA (SATA) connectors (I-SATA 0~5) are located on the motherboard. I-SATA Ports 0/1 support SATA 3.0 (6Mb/s), and I-SATA Ports 2~5 support SATA 2.0 (3 Mb/s). Serial Link connections provide faster data transmission than legacy Parallel ATA. See the table on the right for pin definitions.

| SATA Connectors<br>Pin Definitions |          |
|------------------------------------|----------|
| Pin#                               | Signal   |
| 1                                  | Ground   |
| 2                                  | SATA_TXP |
| 3                                  | SATA_TXN |
| 4                                  | Ground   |
| 5                                  | SATA_RXN |
| 6                                  | SATA_RXP |
| 7                                  | Ground   |

A. I-SATA 0 (3.0)

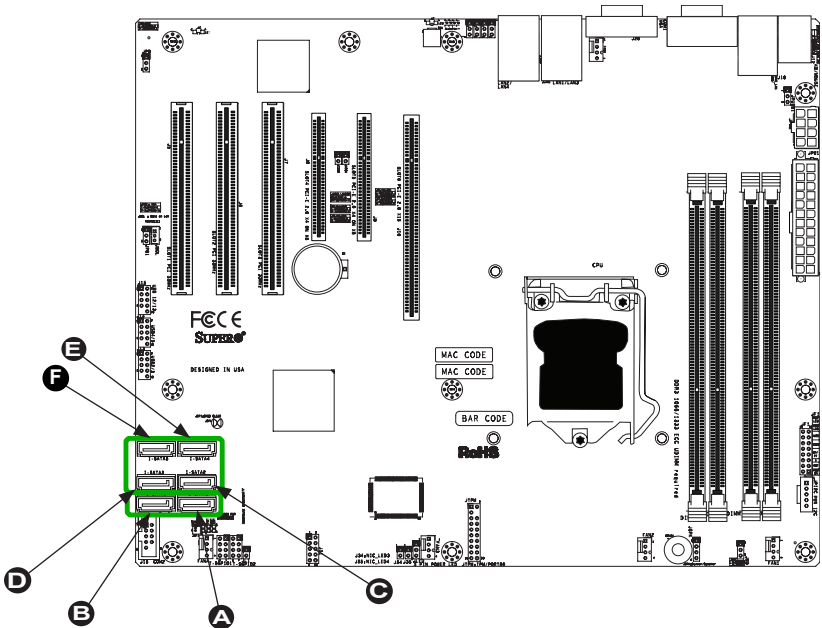
B. I-SATA 1 (3.0)

C. I-SATA 2

D. I-SATA 3

E. I-SATA 4

F. I-SATA 5



## Notes

## Chapter 3

# Troubleshooting

### 3-1 Troubleshooting Procedures

Use the following procedures to troubleshoot your system. If you have followed all of the procedures below and still need assistance, refer to the 'Technical Support Procedures' and/or 'Returning Merchandise for Service' section(s) in this chapter. Always disconnect the AC power cord before adding, changing or installing any hardware components.

#### Before Power On

1. Make sure that the Standby PWR LED (LE2) is not on. (**Note:** If LE2 is on, the onboard power is on. Be sure to unplug the power cable before installing or removing the components.)
2. Make sure that there are no short circuits between the motherboard and chassis.
3. Disconnect all ribbon/wire cables from the motherboard, including those for the keyboard and mouse. Also, be sure to remove all add-on cards.
4. Install a CPU and heatsink (-be sure that it is fully seated) and then connect the chassis speaker and the power LED to the motherboard. Check all jumper settings as well.

#### No Power

1. Make sure that there are no short circuits between the motherboard and chassis.
2. Make sure that all jumpers are set to their default positions.
3. Check if the 115V/230V switch on the power supply is properly set.
4. Turn the power switch on and off to test the system.
5. The battery on your motherboard may be old. Check to make sure that it still supplies ~3VDC. If it does not, replace it with a new one.

## No Video

1. If the power is on, but you have no video--in this case, you will need to remove all the add-on cards and cables first.
2. Use the speaker to determine if any beep codes exist. (Refer to Appendix A for details on beep codes.)
3. Remove all memory modules and turn on the system. (If the alarm is on, check the specs of memory modules, reset the memory or try a different one.)

## Memory Errors

1. Make sure that the DIMM modules are properly installed and fully seated in the slots.
2. You should be using unbuffered ECC DDR3 (1.5V) 1333/1066 MHz memory (recommended by the manufacturer). Also, it is recommended that you use the memory modules of the same type and speed for all DIMMs in the system. Do not use memory modules of different sizes, different speeds and different types on the same motherboard.
3. Check for bad DIMM modules or slots by swapping modules between slots to see if you can locate the faulty ones.
4. Check the switch of 115V/230V power supply.

## Losing the System's Setup Configuration

1. Please be sure to use a high quality power supply. A poor quality power supply may cause the system to lose CMOS setup information. Refer to Section 1-5 for details on recommended power supplies.
2. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.
3. If the above steps do not fix the Setup Configuration problem, contact your vendor for repairs.



## 3-2 Technical Support Procedures

Before contacting Technical Support, please make sure that you have followed all the steps listed below. Also, Note that as a motherboard manufacturer, Supermicro does not sell directly to end users, so it is best to first check with your distributor or reseller for troubleshooting services. They should know of any possible problem(s) with the specific system configuration that was sold to you.

1. Please go through the 'Troubleshooting Procedures' and 'Frequently Asked Question' (FAQ) sections in this chapter or see the FAQs on our website (<http://www.supermicro.com/support/faqs/>) before contacting Technical Support.
2. BIOS upgrades can be downloaded from our website at (<http://www.supermicro.com/support/bios/>).

**Note:** Not all BIOS can be flashed. Some cannot be flashed; it depends on the boot block code of the BIOS.

3. If you've followed the instructions above to troubleshoot your system, and still cannot resolve the problem, then contact Supermicro's technical support and provide them with the following information:
  - Motherboard model and PCB revision number
  - BIOS release date/version (this can be seen on the initial display when your system first boots up)
  - System configuration
  - An example of a Technical Support form is on our website at (<http://www.supermicro.com/support/contact.cfm>).
4. Distributors: For immediate assistance, please have your account number ready when placing a call to our technical support department. We can be reached by e-mail at [support@supermicro.com](mailto:support@supermicro.com), by phone at: (408) 503-8000, option 2, or by fax at (408)503-8019.

### 3-3 Frequently Asked Questions

**Question:** What type of memory does my motherboard support?

**Answer:** The X9SCi-LN4/X9SCi-LN4F/X9SCA/X9SCA-F supports up to 32GB of unbuffered ECC DDR3 SDRAM (1.5V, 1333/1066 MHz). See Section 2-3 for details on installing memory.

**Question:** How do I update my BIOS?

**Answer:** It is recommended that you **do not** upgrade your BIOS if you are not experiencing any problems with your system. Updated BIOS files are located on our website at <http://www.supermicro.com/support/bios/>. Please check our BIOS warning message and the information on how to update your BIOS on our web site. Select your motherboard model and download the BIOS ROM file to your computer. Also, check the current BIOS revision to make sure that it is newer than your BIOS before downloading. Please unzip the BIOS file onto a bootable device or a USB pen/thumb drive. To flash the BIOS, run the batch file named "ami.bat" with the new BIOS ROM file from your bootable device or USB pen/thumb drive. Use the following format:

```
F:\> ami.bat BIOS-ROM-filename.xxx <Enter>
```



**Note:** Always use the file named "ami.bat" to update the BIOS, and insert a space between "ami.bat" and the filename. The BIOS-ROM-filename will bear the motherboard name (i.e., X9SCi) and build version as the extension. For example, "X9SCA1.218". When completed, your system will automatically reboot.

When the BIOS flashing screen is completed, the system will reboot and will show "Press F1 or F2". At this point, you will need to load the BIOS defaults. Press <F1> to go to the BIOS setup screen, and press <F9> to load the default settings. Next, press <F10> to save and exit. The system will then reboot.



**Warning:** Do not shut down or reset the system while updating the BIOS to prevent possible system boot failure!



**Note:** The SPI BIOS chip installed on this motherboard is not removable. To repair or replace a damaged BIOS chip, please send your motherboard to RMA at Supermicro for service.

**Question:** I think my BIOS is corrupted. How can I recover my BIOS?

**Answer:** Please see Appendix C-BIOS Recovery for detailed instructions.

**Question:** What's on the CD that came with my motherboard?

**Answer:** The supplied compact disc has quite a few drivers and programs that will greatly enhance your system. We recommend that you review the CD and install the

applications you need. Applications on the CD include chipset drivers for Windows, security programs, and audio drivers.

**Question:** Why do I get an error message "IASTOR.SYS read error" and "press F6 to install Intel RAID driver" when installing Windows on my motherboard?

**Answer:** To solve this issue, disable the IPMI jumper. Another solution is to use a USB floppy drive instead of the onboard floppy drive. For the IPMI jumper location, please check Chapter 1.

**Question:** What is the heatsink part number for my X9SCi-LN4/X9SCi-LN4F/X9SCA/X9SCA-F motherboard?

**Answer:** For the 1U passive heatsink, ask for SNK-P0046P (back plate is included). For the 2U active heatsink, use SNK-P0046A4.

**Question:** Why can't I recover the BIOS even when I've followed the instructions in the user's manual for the motherboard?

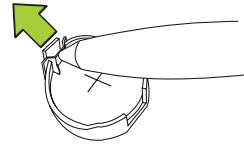
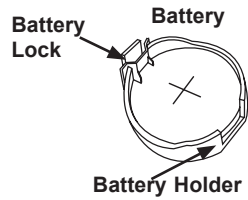
**Answer:** Please disable the IPMI jumper and try it again. For the jumper location, please check Chapter 1.

### 3-4 Battery Removal and Installation

#### Battery Removal

To remove the onboard battery, follow the steps below:

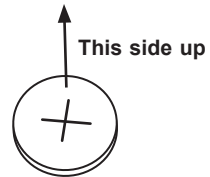
1. Power off your system and unplug your power cable.
2. Locate the onboard battery as shown below.
3. Using a tool such as a pen or a small screwdriver, push the battery lock outwards to unlock it. Once unlocked, the battery will pop out from the holder.
4. Remove the battery.



#### Proper Battery Disposal

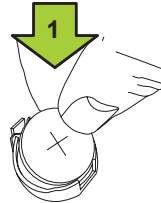


**Warning!** Please handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

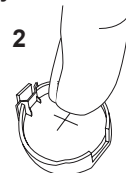


#### Battery Installation

1. To install an onboard battery, follow the steps 1 & 2 above and continue below:
2. Identify the battery's polarity. The positive (+) side should be facing up.
3. Insert the battery into the battery holder and push it down until you hear a click to ensure that the battery is securely locked.



**Press down until you hear a click.**



**Warning:** When replacing a battery, be sure to only replace it with the same type.

### **3-5 Returning Merchandise for Service**

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. For faster service, you may also obtain RMA authorizations online (<http://www.supermicro.com/support/rma/>). When you return the motherboard to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton, and mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alteration, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.

## Notes

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## Chapter 4

### BIOS

#### 4-1 Introduction

This chapter describes the AMI BIOS Setup Utility for the X9SCi-LN4/X9SCi-LN4F/X9SCA/X9SCA-F. The AMI ROM BIOS is stored in a Flash EEPROM and can be easily updated. This chapter describes the basic navigation of the AMI BIOS Setup Utility setup screens.



**Note:** For instructions on BIOS recovery, please refer to the instruction guide posted at <http://www.supermicro.com/support/manuals/>.

#### Starting BIOS Setup Utility

To enter the AMI BIOS Setup Utility screens, press the <Delete> key while the system is booting up.



**Note:** In most cases, the <Delete> key is used to invoke the AMI BIOS setup screen. There are a few cases when other keys are used, such as <F1>, <F2>, etc.

Each main BIOS menu option is described in this manual. The Main BIOS setup menu screen has two main frames. The left frame displays all the options that can be configured. Grayed-out options cannot be configured. Options in blue can be configured by the user. The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it. (**Note:** the AMI BIOS has default text messages built in. Supermicro retains the option to include, omit, or change any of these text messages.)

The AMI BIOS Setup Utility uses a key-based navigation system called "hot keys". Most of the AMI BIOS setup utility "hot keys" can be used at any time during the setup navigation process. These keys include <F1>, <F10>, <Enter>, <ESC>, arrow keys, etc.



**Note:** Options printed in **Bold** are default settings.

#### How To Change the Configuration Data

The configuration data that determines the system parameters may be changed by entering the AMI BIOS Setup utility. This Setup utility can be accessed by pressing <Del> at the appropriate time during system boot.

## How to Start the Setup Utility

Normally, the only visible Power-On Self-Test (POST) routine is the memory test. As the memory is being tested, press the <Delete> key to enter the main menu of the AMI BIOS Setup Utility. From the main menu, you can access the other setup screens. An AMI BIOS identification string is displayed at the left bottom corner of the screen, below the copyright message.



**Warning!** Do not upgrade the BIOS unless your system has a BIOS-related issue. Flashing the wrong BIOS can cause irreparable damage to the system. In no event shall Supermicro be liable for direct, indirect, special, incidental, or consequential damages arising from a BIOS update. If you have to update the BIOS, do not shut down or reset the system while the BIOS is updating. This is to avoid possible boot failure.

## 4-2 Main Setup

When you first enter the AMI BIOS Setup Utility, you will enter the Main setup screen. You can always return to the Main setup screen by selecting the Main tab on the top of the screen. The Main BIOS Setup screen is shown below.



**System Overview:** The following BIOS information will be displayed:

### **System Time/System Date**

Use this option to change the system time and date. Highlight *System Time* or *System Date* using the arrow keys. Enter new values through the keyboard. Press the <Tab> key or the arrow keys to move between fields. The date must be entered in Day MM/DD/YY format. The time is entered in HH:MM:SS format. (**Note:** The time is in the 24-hour format. For example, 5:30 P.M. appears as 17:30:00.)

Supermicro X9SCi-LN4/X9SCi-LN4F/X9SCA/X9SCA-F

Version

Build Date

### **Processor**

The AMI BIOS will automatically display the status of processor as shown below:

Processor

Speed

Physical Count

Logical Count

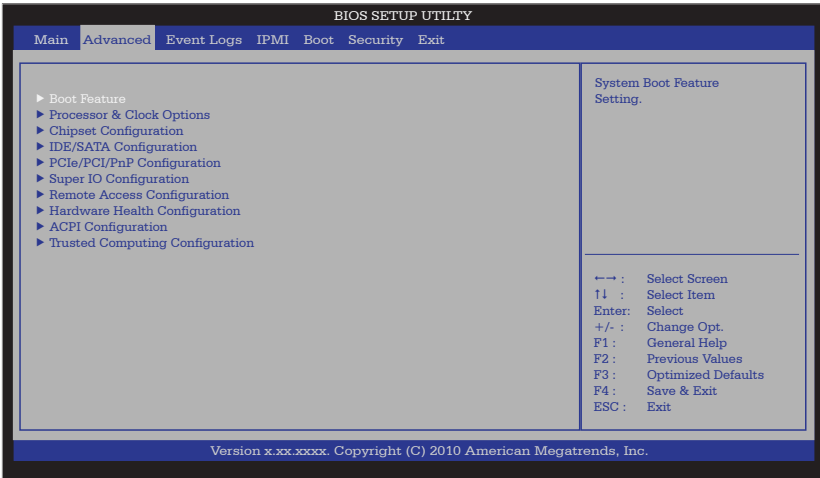
### **System Memory**

This displays the size of memory available in the system:

Size

## 4-3 Advanced Setup Configurations

Use the arrow keys to select Boot Setup and hit <Enter> to access the submenu items:



### ►BOOT Feature

#### Quiet Boot

This option allows the bootup screen options to be modified between POST messages or the OEM logo. Select Disabled to display the POST messages. Select Enabled to display the OEM logo instead of the normal POST messages. The options are **Enabled** and Disabled.

#### AddOn ROM Display Mode

This sets the display mode for Option ROM. The options are **Force BIOS** and Keep Current.

#### Bootup Num-Lock

This feature selects the Power-on state for Numlock key. The options are Off and **On**.

#### Wait For 'F1' If Error

This forces the system to wait until the 'F1' key is pressed if an error occurs. The options are Disabled and **Enabled**.

#### Interrupt 19 Capture

Interrupt 19 is the software interrupt that handles the boot disk function. When this item is set to Enabled, the ROM BIOS of the host adaptors will "capture" Interrupt

19 at boot and allow the drives that are attached to these host adaptors to function as bootable disks. If this item is set to Disabled, the ROM BIOS of the host adaptors will not capture Interrupt 19, and the drives attached to these adaptors will not function as bootable devices. The options are **Enabled** and Disabled.

### Watch Dog Function

If enabled, the Watch Dog Timer will allow the system to reboot when it is inactive for more than 5 minutes. The options are Enabled and **Disabled**.

### Power Button Function

This feature controls how the system shuts down when the power button is pressed. Select 4-Second Override to force the user to press and hold the Power Button for 4 seconds before the system turns off. Select Instant Off if you want the system to instantly power off when the Power Button is pressed. The options are 4 Second Override and **Instant Off**.

### Restore on AC Power Loss

Use this feature to set the power state after a power outage. Select Power-Off for the system power to remain off after a power loss. Select Power-On for the system power to be turned on after a power loss. Select Last State to allow the system to resume its last state before a power loss. The options are Power-On, Power-Off and **Last State**.

### Deep Sx

Select Enabled to enable Deep Sleep State support. The settings are Enabled and **Disabled**.

## ►Processor & Clock Options



**Warning:** Take Caution when changing the Advanced settings. An incorrect value, a very high DRAM frequency or incorrect DRAM timing may cause system to become unstable. When this occurs, revert to the default setting.

### Clock Spread Spectrum

Select Enable to use the feature of Clock Spectrum, which will allow the BIOS to monitor and attempt to reduce the level of Electromagnetic Interference caused by the components whenever needed. Select Disabled to enhance system stability. The options are **Disabled** and Enabled.

### Hardware Prefetcher (Available when supported by the CPU)

If set to Enabled, the hardware pre fetcher will pre fetch streams of data and instructions from the main memory to the L2 cache in the forward or backward manner to improve CPU performance. The options are Disabled and **Enabled**.

---

### **Adjacent Cache Line Prefetch (Available when supported by the CPU)**

The CPU fetches the cache line for 64 bytes if this option is set to Disabled. The CPU fetches both cache lines for 128 bytes as comprised if **Enabled**.

### **Intel® Virtualization Technology (Available when supported by the CPU)**

Select Enabled to use the feature of Virtualization Technology to allow one platform to run multiple operating systems and applications in independent partitions, creating multiple "virtual" systems in one physical computer. The options are **Enabled** and Disabled. **Note:** If there is any change to this setting, you will need to power off and restart the system for the change to take effect. Please refer to Intel's web site for detailed information.

### **Execute-Disable Bit Capability (Available when supported by the OS and the CPU)**

Set to Enabled to enable the Execute Disable Bit which will allow the processor to designate areas in the system memory where an application code can execute and where it cannot, thus preventing a worm or a virus from flooding illegal codes to overwhelm the processor or damage the system during an attack. The default is **Enabled**. (Refer to Intel and Microsoft Web Sites for more information.)

### **Intel® Hyper Threading Technology**

Set to Enabled to use the processor's Hyper Threading Technology feature. The options are **Enabled** and Disabled.

### **Active Processor Cores**

Set to Enabled to use a processor's Second Core and beyond. (Please refer to Intel's web site for more information.) The options are **All**, 1, 2 and 3.

### **Power Technology**

This feature determines what power-saving scheme the motherboard uses. The options are Disabled, **Energy Efficient** and Custom. If Custom is selected, the following options become available:

#### **EIST**

EIST (Enhanced Intel SpeedStep Technology) allows the system to automatically adjust processor voltage and core frequency in an effort to reduce power consumption and heat dissipation. **Please refer to Intel's web site for detailed information.** The options are **Disabled** and Enabled.

#### **P-STATE Coordination**

This feature selects the type of coordination for the P-State of the processor. P-State is a processor operational state that reduces the processor's voltage

and frequency. This makes the processor more energy efficient, resulting in further gains. The options are **HW\_ALL**, **SW\_ALL** and **SW-ANY**.

### **CPU C3 Report, CPU C6 Report**

This BIOS feature enables or disables C3 (ACPI C2) or C6 (ACPI C3) reporting to the operating system. The options are **Disabled** and **Enabled**.

### **C-State package limit setting**

If set to Auto, the AMI BIOS will automatically set the limit on the C-State package register. The options are **C0**, **C1**, **C6**, **C7** and **No Limit**.

## **►Intel® Turbo Boost Technology (Available if Intel® EIST technology is Enabled)**

### **Turbo Mode**

This feature allows processor cores to run faster than marked frequency in specific conditions. The options are **Disabled** and **Enabled**.

**Long duration power limit** - this is the processor power consumption limit (in Watts) during a long duration time window.

**Long duration maintained** - this is the time in milliseconds where the Long Duration Power Limit is maintained.

**Short duration power limit** - During Turbo Mode, the system may exceed the processor's default power setting and exceed the Short Duration Power limit. By increasing this value, the processor can provide better performance for a short duration.

## **►Chipset Configuration**

**WARNING:** Setting the wrong values in the following sections may cause the system to malfunction.

### **►CPU Bridge Configuration**

This item displays the current CPU Revision, Current CPU1 Memory Frequency, Memory Type and Memory Reference Code Revision.

#### **Memory Frequency**

This feature allows the user to select the memory speed. Under normal conditions, please set this to Auto. The options are **Auto**, **Force DDR-800**, **Force DDR-1067**, **Force DDR-1333**, and **Force DDR-1600**.

### **►Integrated IO Configuration**

This item displays the current IO chipset Revision.

### **VT-d**

Select Enabled to enable Intel's Virtualization Technology support for Direct I/O VT-d by reporting the I/O device assignments to VMM through the DMAR ACPI Tables. This feature offers fully-protected I/O resource-sharing across the Intel platforms, providing the user with greater reliability, security and availability in networking and data-sharing. The settings are Enabled and **Disabled**.

### **Active State Power Management**

Select Enabled to start Active-State Power Management for signal transactions between L0 and L1 Links on the PCI Express Bus. This maximizes power-saving and transaction speed. The options are Enabled and **Disabled**.

### **PCIE Maximum Payload Size**

This feature selects the setting for the PCIE maximum payload size. The options are **Auto**, 128 Bytes, 256 Bytes, 512 Bytes, 1024 Bytes, 2048 Bytes, and 4096 Bytes.

### **PCI Express Port**

This feature enables or disables the PCI Express port. The options are Disabled, Enabled and **Auto**.

### **PEG Force Gen1**

This feature forces Gen1 support on the PCI Express Graphics (PEG) port. The options are **Disabled**, and Enabled.

### **Detect Non-Compliant Device**

This feature enables or disables the detection of a non-compliant device that is attached to the PCI Express Graphics (PEG) port. The options are **Disabled**, and Enabled.

## **► South Bridge Configuration**

This item displays the current South Bridge Revision.

### **USB Functionst**

This feature enables configuration of USB parameters. The options are **Enabled**, and Disabled. If set to Enabled, the follow options appear:

### **Legacy USB Support**

This feature enables support for legacy USB devices. Select Auto to disable legacy support if USB devices are not present. Select Disabled to have USB devices available only for EFI applications. The options are **Enabled**, Disabled and Auto.

### **BIOS EHCI Hand-Off**

This item is for Operating Systems that does not support Enhanced Host Controller Interface (EHCI) hand-off. When enabled, EHCI ownership change will be claimed by the EHCI driver. The settings are **Enabled** and Disabled.

## **►IDE/SATA Configuration**

When this submenu is selected, the AMI BIOS automatically detects the presence of the IDE Devices and displays the following items:

### **SATA Mode**

This item selects the mode for the installed drives. The options are Disabled, IDE Mode, **AHCI Mode** and RAID Mode.

#### **IDE Mode**

The following items are displayed when IDE Mode is selected:

#### **Serial-ATA Controller 0~1**

This feature is used to activate/deactivate the SATA controller, and sets the compatibility mode. The options are Enhanced and **Compatible**. The default of Serial-ATA Controller 1 is **Enhanced**.

#### **SATA Port0~Port5**

This item displays the information detected on the installed SATA drives on the particular SATA port.

#### **AHCI Mode**

The following items are displayed when AHCI Mode is selected:

#### **Aggressive Link Power Management**

This feature Enables or Disables Aggressive Link Power Management support for Cougar Point B0 stepping and later. The options are **Enabled** and Disabled.

### **SATA Port0~Port5**

This item displays the information detected on the installed SATA drives on the particular SATA port.

#### **Staggered Spin Up**

Set this item to Enabled to enable Staggered Spin-up support. The options are Enabled and **Disabled**.

#### **External SATA Port**

Set this item to Enabled to enable eSATA support. The options are Enabled and **Disabled**.

#### **Hot Plug**

Set this item to Enabled to enable hot-plugging. The options are Enabled and **Disabled**.

### **RAID Mode**

The following items are displayed when RAID Mode is selected:

#### **SATA Port0~Port5**

This item displays the information detected on the installed SATA drives on the particular SATA port.

#### **Hot Plug**

Set this item to Enabled to enable hot-plugging. The options are Enabled and **Disabled**.

## **►PCIe/PCI/PnP Configuration**

This feature allows the user to set the PCI/PnP configurations for the following items:

### **PCI ROM Priority**

In case of multiple Option ROMs (Legacy and EFI-compatible), this feature specifies what ROM to launch. The options are **Legacy ROM** and EFI Compatible ROM.

### **PCI Latency Timer**

This feature sets the latency Timer of each PCI device installed on a PCI bus. Select 64 to set the PCI latency to 64 PCI clock cycles. The options are 32, **64**, 96, 128, 160, 192, 224 and 248.



### **SR-IOV Support**

Single Root I/O Virtualization (SR-IOV) is a specification that allows a PCIe device to appear as multiple physical devices to the system. The options are **Disabled** and **Enabled**.

### **PCI-E Slot 4, 5, 6, & 7 OPROM**

Use this feature to enable or disable PCI slot Option ROMs. The options are **Disabled** and **Enabled**.

### **Onboard LAN Option ROM Select**

This feature selects whether to load the iSCSI or PXE onboard LAN option ROM. The options are **iSCSI** and **PXE**.

### **Load Onboard LAN1 Option ROM/Load Onboard LAN2 Option ROM**

This feature is to enable or disable the onboard option ROMs. The options are **Disabled** and **Enabled**.

## **►Super IO Device Configuration**

### **Serial Port 1 / Serial Port 2**

Select **Enabled** to enable the onboard serial port. The options are **Enabled** and **Disabled**.

### **Serial Port1 Settings/ Serial Port2 Settings**

This option specifies the base I/O port address and the Interrupt Request address of Serial Port 1 and Serial Port 2. Select **Disabled** to prevent the serial port from accessing any system resources. When this option is set to **Disabled**, the serial port physically becomes unavailable. Select **3F8/IRQ4** to allow the serial port to use 3F8 as its I/O port address and IRQ 4 for the interrupt address. The options for Serial Port1 are **Disabled**, **3F8/IRQ4**, **3E8/IRQ4**, **2F8/IRQ3** and **2E8/IRQ3**. The options for Serial Port2 are **Disabled**, **3F8/IRQ4**, **3E8/IRQ4**, **2F8/IRQ3**, and **2E8/IRQ3**.

## **►Remote Access Configuration**

### **COM0/COM1/SOL Console Redirection**

Use this feature to enable console redirection for COM0 and COM1 ports. The options are **Enabled** and **Disabled**. The default for all ports are **Disabled**.

#### **►Console Redirection Settings**

Configure the following options for the Console Redirection Settings. The most common settings are set as default:

Terminal Type : Select **ANSI**, VT100, VT100+, or VT-UTF8

Bits per Second (BPS): 9600, 19200, 57600, or **115200**

Data Bits: **8** or 7

Parity: **None**, Even, Odd, Mark, or Space

Stop Bits: **1** or 2

Flow Control: **None** or Hardware RTS/CTS

Recorder Mode: **Disabled** or Enabled

Resolution 100x31: **Disabled** or Enabled

Legacy OS Redirection Resolution: **80x24** or 80x25

### **Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS)**

Use this feature to enable console redirection. The options are **Enabled** and Disabled. The default is Disabled.

#### **► Console Redirection Settings**

Configure the following options for the Console Redirection Settings. The most common settings are set as default:

Out-of-Band Mgmt Port: **COM1**, COM2, SOL, COM4

Terminal Type : Select ANSI, VT100, VT100+, or **VT-UTF8**

Bits per Second (BPS): 9600, 19200, 57600, or **115200**

Data Bits: **8** or 7

Parity: **None**, Even, Odd, Mark, or Space

Stop Bits: **1** or 2

#### **► Hardware Health Configuration**

##### **Fan Speed Control Mode**

This feature allows the user to decide how the system controls the speeds of the onboard fans. The CPU temperature and the fan speed are correlative. When the CPU on-die temperature increases, the fan speed will also increase for effective system cooling. Select "Full Speed" to allow the onboard fans to run at full speed (of 100% Pulse Width Modulation Duty Cycle) for maximum cooling. This setting is recommended for special system configuration or debugging. Select "Standard" for the onboard fans to run at 50% of the Initial PWM Cycle in order to balance the needs between system cooling and power saving. This setting is recommended for regular systems with normal hardware configurations. Select "Optimal" for the

onboard fans to run at 30% of the Initial PWM Cycle for best power efficiency and maximum quietness. The options are Full Speed (@100% of PWM Cycle), **Standard** (@50% of PWM Cycle), and Optimal (@30% of PWM Cycle).

### **CPU Temperature Display Mode**

This feature displays the CPU temperature detected by DTS (i.e., +34°C) or temperature status in text ("Low", "Medium" or "High"). The options are **Text Mode** or DTS.

If Text Mode is selected, the CPU Temperature Display Mode will show the CPU temperature status as follows:

Low – This level is considered as the 'normal' operating state. The CPU temperature is well below the CPU 'Temperature Tolerance'. The motherboard fans and CPU will run normally as configured in the BIOS (Fan Speed Control).

User intervention: No action required.

Medium – The processor is running warmer. This is a 'precautionary' level and generally means that there may be factors contributing to this condition, but the CPU is still within its normal operating state and below the CPU 'Temperature Tolerance'. The motherboard fans and CPU will run normally as configured in the BIOS. The fans may adjust to a faster speed depending on the Fan Speed Control settings.

User intervention: No action is required. However, consider checking the CPU fans and the chassis ventilation for blockage.

High – The processor is running hot. This is a 'caution' level since the CPU's 'Temperature Tolerance' has been reached (or has been exceeded) and may activate an overheat alarm:

The information provided above is for your reference only. For more information on thermal management, please refer to Intel's Web site at [www.Intel.com](http://www.Intel.com).

### **Fan1 ~ Fan4, FanA Reading**

This feature displays the fan speed readings from fan interfaces Fan1 through Fan4 and FanA.

### **12V, VDIMM, 5VCC, -12V, AVCC, 3.3VCC, VSB, VBAT**

This feature displays the current voltages of the above voltage monitors.

## ►ACPI Configuration

Use this feature to configure Advanced Configuration and Power Interface (ACPI) power management settings for your system.

### High Precision Event Timers

Select Enabled to activate the High Performance Event Timer (HPET) that produces periodic interrupts at a much higher frequency than a Real-time Clock (RTC) does in synchronizing multimedia streams, providing smooth playback and reducing the dependency on other timestamp calculation devices, such as an x86 RDTSC Instruction embedded in the CPU. The High Performance Event Timer is used to replace the 8254 Programmable Interval Timer. The options are **Enabled** and Disabled.

### Suspend Mode

This setting allows you to configure the ACPI (Advanced Configuration and Power Interface) sleep state for your system when it is in the Suspend mode. The options are Suspend Disabled and **S1 (POS)**.

### NUMA Support

This feature Enables the Non-Uniform Memory Access ACPI support. The options are **Enabled** and Disabled.

### WHEA Support

This feature Enables the Windows Hardware Error Architecture (WHEA) support for the Windows 2008 operating system (and later versions). The options are **Enabled** and Disabled.

## 4-4 Event Logs

### Smbios Event Log

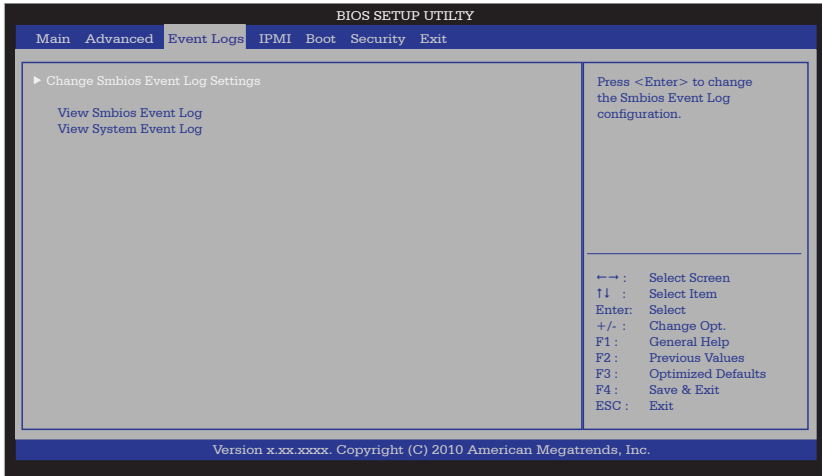
Change this item to enable or disable all features of the Smbios Event Logging during boot. The options are **Enabled** and Disabled.

### Erase Event Log

This option erases all logged events. The options are **No**, Yes, Next reset and Yes, Every reset.

### When Log is Full

This option automatically clears the Event Log memory of all messages when it is full. The options are **Do Nothing** and Erase Immediately.



### MECI

The Multiple Event Count Increment (MECI) counter counts the number of occurrences a duplicate event must happen before the MECI counter is incremented. This is a numeric value.

### METW

The Multiple Event Time Window (METW) defines number of minutes must pass between duplicate log events before MECI is incremented. This is in minutes, from 0 to 99.

## 4-5 IPMI Configuration (X9SCi-LN4F, X9SCA-F Only)

Intelligent Platform Management Interface (IPMI) is a set of common interfaces that IT administrators can use to monitor system health and to manage the system as a whole. For more information on the IPMI specifications, please visit Intel's website at [www.intel.com](http://www.intel.com).

### BMC Support

This feature enables or disables the installed Baseboard Management Controller (BMC) on the motherboard. The options are **Enabled** and Disabled.

### Wait For BMC

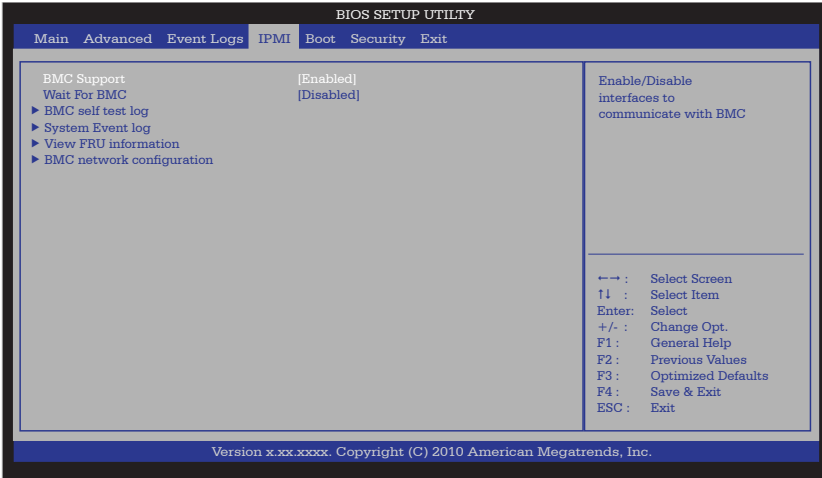
If enabled, the system will wait for a BMC response after the AC power is turned on. It takes around 30 seconds to initialize Host to BMC interfaces. The options are **Disabled** and Enabled.

► **BMC Self Test Log**

This feature logs any BMC messages returned during a BMC self-test. It shows the total number of entries and will allow the viewing of each event by scrolling down.

**Erase Log** - Select **Yes, On every reset** or **No**.

**When Log is Full** - Select **Clear Log** or **Do Not Log Anymore**.



► **System Event Log**

This feature is used to change the Sytem Event Log (SEL) configuration.

**SEL Components** - Change this item to enable or disable all features of System Event Logging. The options are Enabled and Disabled. When Enabled, the following can be configured:

**Erase SEL** - This option erases all logged SEL events. The options are No, Yes, On Next reset and Yes, On Every reset.

**When SEL Full**

This option automatically clears the System Event Log memory of all messages when it is full. The options are **Do Nothing** and **Erase Immediately**.

**Log EFI Status Codes**

This option enables or disables the logging of Extensible Firmware Interface (EFI) status codes. The options are Disabled, **Both**, Error Code and Progress Code.

► **View FRU Information**

This is an informational item which displays the information pertaining to this motherboard (FRU - Field Replaceable Unit).

### ►BMC Network Configuration

Set this feature to configure the IPMI LAN adapter with a network address.

#### Configuration Source

This feature selects whether the IP address, Subnet Mask and Gateway Address are automatically assigned by the network's DHCP server (Dynamic Host and Configuration Protocol) "Dynamic" or manually entered by the user "Static". If Static is selected, the IP Address, Subnet Mask and Gateway Address must be manually entered below. The options are Static, Dynamic and **Do Nothing**. The following items are displayed when Static is selected:

**Station IP Address** - Enter the IP address for this machine. This should be in decimal and in dotted quad form (i.e., 192.168.10.253). The value of each three-digit number separated by dots should not exceed 255.

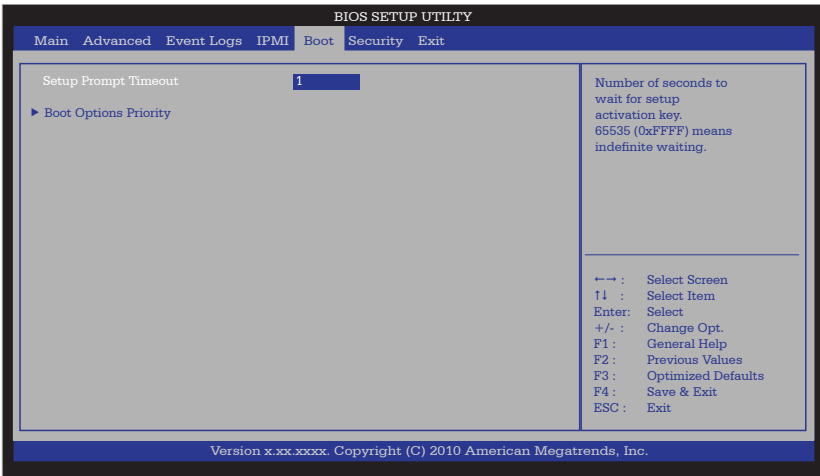
**Subnet Mask** - Subnet masks tell the network which subnet this machine belongs to. The value of each three-digit number separated by dots should not exceed 255.

**Station MAC Address** - MAC addresses are 6 two-digit hexadecimal numbers (Base 16, 0 ~ 9, A, B, C, D, E, F) separated by dots (i.e., 00.30.48.D0.D4.60).

**Router IP Address** - Enter the Gateway or Router address this machine will use (i.e., 192.168.10.1).

**Router MAC Address** - Enter the MAC address of the Router or Gateway (only if needed).

## 4-6 Boot Settings



Use this feature to configure Boot Settings:

### ▶ Boot Options Priority

This feature allows the user to specify which devices are boot devices and the order of priority from which the systems boots from during startup.

#### Boot Option #1, Boot option #2, Boot Option #3, etc

The settings are **Built-in EFI Shell**, [any detected boot device] and Disabled.

### ▶ Delete Boot Option

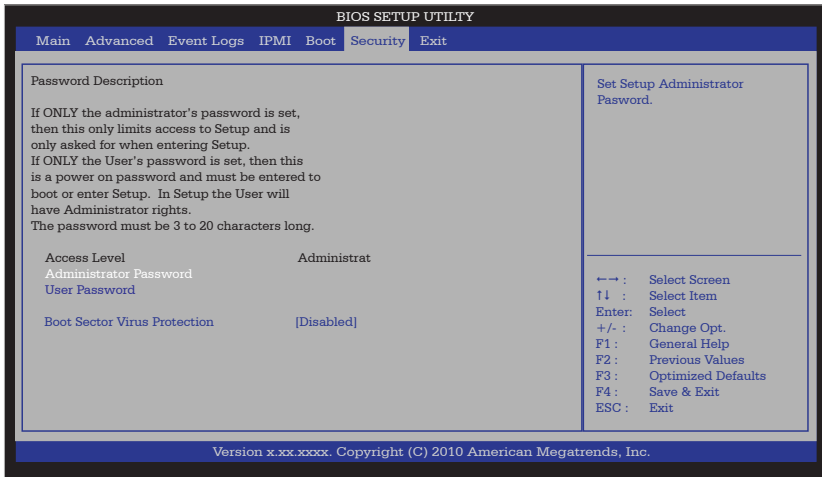
This feature allows the user to delete a previously defined boot device from which the systems boots from during startup.

#### Boot Option #1, Boot option #2, Boot Option #3, etc

The settings are **Built-in EFI Shell**, and [any pre defined boot device]



## 4-7 Security Settings



- If the Administrator password is defined ONLY - this controls access to the BIOS setup ONLY.
- If the User's password is defined ONLY - this password will need to be entered during each system startup or boot, and will also have Administrator rights in the setup.
- Passwords must be at least 3 and up to 20 characters long.

### Administrator Password

Press Enter to create a new, or change an existing Administrator password.

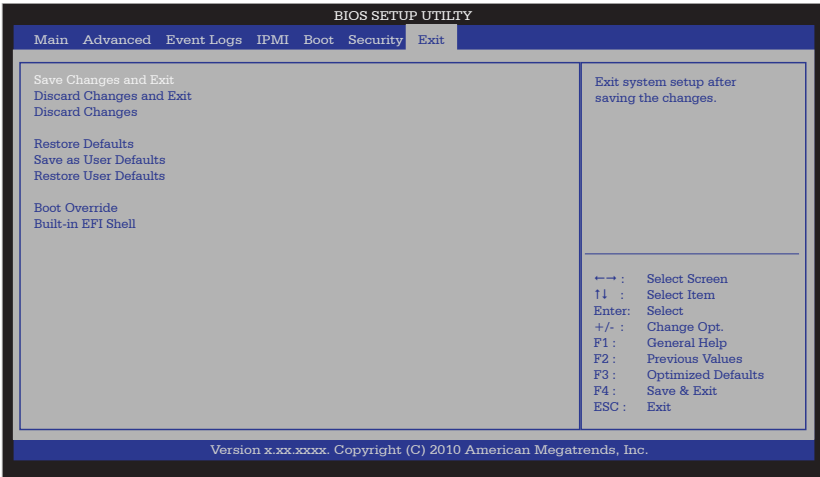
### User Password:

Press Enter to create a new, or change an existing User password.

### Boot Sector Virus Protection

When Enabled, the BIOS displays a warning when any program (or virus) issues a Disk Format command or attempts to write to the boot sector of the hard disk drive. The options are Enabled and **Disabled**.

## 4-8 Exit Options



Select the Exit tab from the BIOS Setup Utility screen to enter the Exit BIOS Setup screen.

### Save Changes and Exit

When you have completed the system configuration changes, select this option to leave the BIOS Setup Utility and reboot the computer, so the new system configuration parameters can take effect. Select Save Changes and Exit from the Exit menu and press <Enter>.

### Discard Changes and Exit

Select this option to quit the BIOS Setup without making any permanent changes to the system configuration, and reboot the computer. Select Discard Changes and Exit from the Exit menu and press <Enter>.

### Discard Changes

Select this option and press <Enter> to discard all the changes and return to the AMI BIOS Utility Program.

### Restore Defaults

To set this feature, select Restore Defaults from the Exit menu and press <Enter>. These are factory settings designed for maximum system stability, but not for maximum performance.

### **Save As User Defaults**

To set this feature, select Save as User Defaults from the Exit menu and press <Enter>. This enables the user to save any changes to the BIOS setup for future use

### **Restore User Defaults**

To set this feature, select Restore User Defaults from the Exit menu and press <Enter>. Use this feature to retrieve user-defined settings that were saved previously.

### **Boot Override**

Set this feature to override a previously defined boot device. The available devices will be listed below.

## Notes

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## Appendix A

### BIOS Error Beep Codes

During the POST (Power-On Self-Test) routines, which are performed each time the system is powered on, errors may occur.

**Non-fatal errors** are those which, in most cases, allow the system to continue with bootup. The error messages normally appear on the screen.

**Fatal errors** will not allow the system to continue to bootup. If a fatal error occurs, you should consult with your system manufacturer for possible repairs.

These fatal errors are usually communicated through a series of audible beeps. The numbers on the fatal error list correspond to the number of beeps for the corresponding error.

#### A-1 BIOS Error Beep Codes

| BIOS Error Beep Codes       |                                 |  |
|-----------------------------|---------------------------------|--|
| Beep Code/LED               | Error Message                   | Description                                      |
| 1 beep                      | Refresh                         | Circuits have been reset.<br>(Ready to power up) |
| 5 short beeps + 1 long beep | Memory error                    | No memory detected in the system                 |
| 8 beeps                     | Display memory read/write error | Video adapter missing or with faulty memory      |
| OH LED On                   | System OH                       | System Overheat                                  |

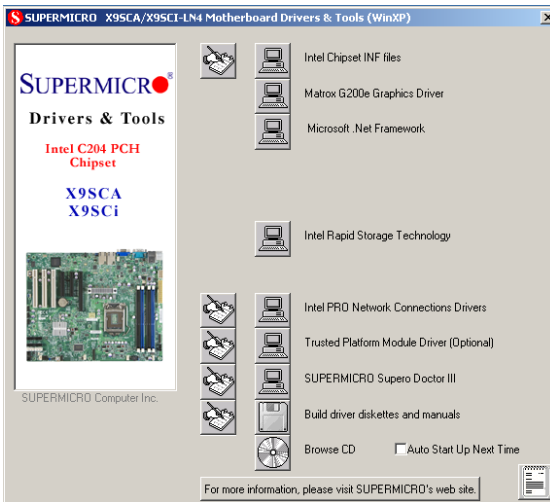
## Notes

## Appendix B

### Software Installation Instructions

#### B-1 Installing Drivers

After you've installed the Windows Operating System, a screen as shown below will appear. You are ready to install software programs and drivers that have not yet been installed. To install these software programs and drivers, click the icons to the right of these items. (**Note:** To install the Windows Operating System, please refer to the instructions posted on our website at [http://www.supermicro.com/support/manuals/.](http://www.supermicro.com/support/manuals/))



#### Driver/Tool Installation Display Screen



**Note 1.** Click the icons showing a hand writing on the paper to view the readme files for each item. Click on a computer icon to the right of an item to install this item (from top to the bottom), one at a time. After installing each item, you must reboot the system before proceeding with the next item on the list. The bottom icon with a CD on it allows you to view the entire contents of the CD.

**Note 2.** When making a storage driver diskette by booting into a Driver CD, please set the SATA Configuration to "Compatible Mode" and configure SATA as IDE in the BIOS Setup. After making the driver diskette, be sure to change the SATA settings back to your original settings.

## B-2 Configuring SuperDoctor<sup>®</sup> III

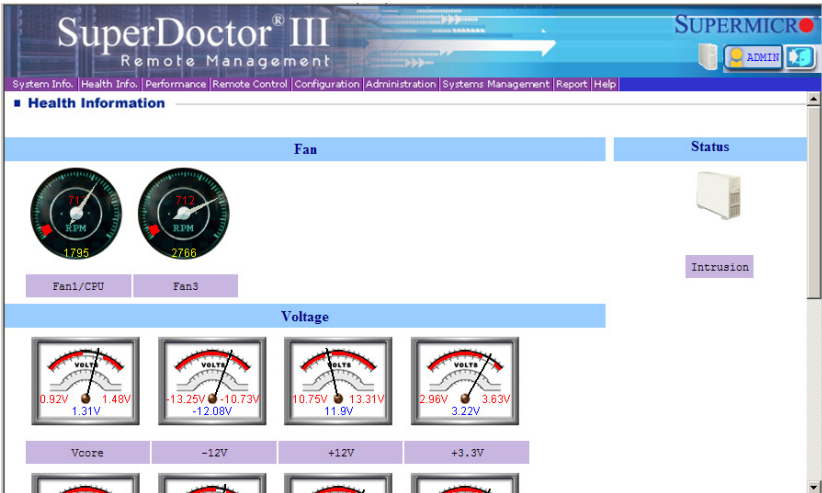
The SuperDoctor III program is a Web-based management tool that supports remote management capability. It includes Remote and Local Management tools. The local management tool is called the SD III Client. The SuperDoctor III program included on the CDROM that came with your motherboard allows you to monitor the environment and operations of your system. SuperDoctor III displays crucial system information such as CPU temperature, system voltages and fan status. See the Figure below for a display of the SuperDoctor III interface.



**Note:** 1 The default user name and password are ADMIN.

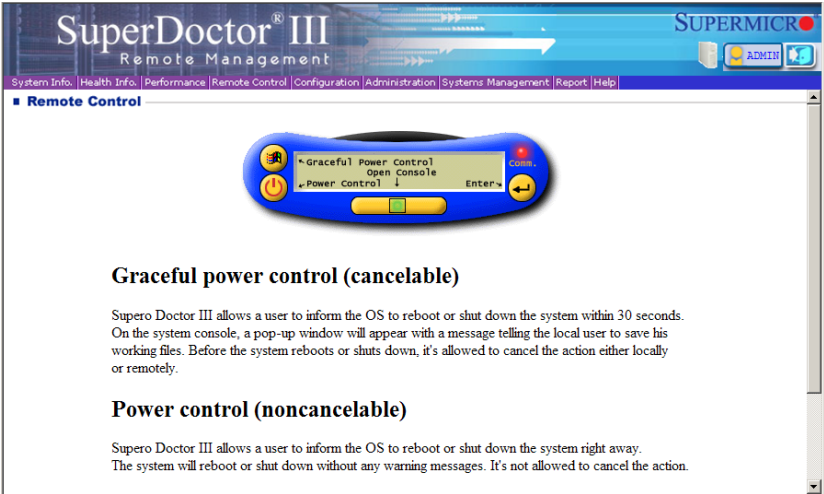
**Note 2:** In the Windows OS environment, the SuperDoctor III settings take precedence over the BIOS settings. When first installed, SuperDoctor III adopts the temperature threshold settings previously set in BIOS. Any subsequent changes to these thresholds must be made within SuperDoctor, since the settings override the BIOS settings. For the Windows OS to adopt the BIOS temperature threshold settings, please change the SD III Client settings to be the same as those set in BIOS.

**SuperDoctor III Interface Display Screen-I (Health Information)**





## SuperDoctor III Interface Display Screen-II (Remote Control)



**Graceful power control (cancelable)**

Super Doctor III allows a user to inform the OS to reboot or shut down the system within 30 seconds. On the system console, a pop-up window will appear with a message telling the local user to save his working files. Before the system reboots or shuts down, it's allowed to cancel the action either locally or remotely.

**Power control (noncancelable)**

Super Doctor III allows a user to inform the OS to reboot or shut down the system right away. The system will reboot or shut down without any warning messages. It's not allowed to cancel the action.



**Note:** The SuperDoctor III software and manual may be downloaded from our Website at:

<http://www.supermicro.com/products/accessories/software/SuperDoctorIII.cfm>.

For Linux, we still recommend that you use SuperDoctor II, this version is also available for download at the link above.

## Notes

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## Appendix C

### UEFI BIOS Recovery Instructions



**Warning!** Do not upgrade the BIOS unless your system has a BIOS-related issue. Flashing the wrong BIOS can cause irreparable damage to the system. In no event shall Supermicro be liable for direct, indirect, special, incidental, or consequential damages arising from a BIOS update. If you need to update the BIOS, do not shut down or reset the system while the BIOS is updating to avoid possible boot failure.

#### An Overview to the UEFI BIOS

The Unified Extensible Firmware Interface (UEFI) specification provides a software-based interface between the operating system and the platform firmware in the pre-boot environment. The UEFI specification supports an architecture-independent mechanism for add-on card initialization to allow the UEFI OS loader, which is stored in the add-on card, to boot up the system. UEFI offers a clean, hand-off control to a computer system at bootup.

#### How to Recover the UEFI BIOS Image (-the Main BIOS Block)

An AMIBIOS flash chip consists of a boot sector block and a main BIOS code block (a main BIOS image). The boot sector block contains critical BIOS codes, including memory detection and recovery codes for the user to flash a new BIOS image if the original BIOS image is corrupted. When the system power is on, the boot sector codes execute first. Once it is completed, the main BIOS code will continue with system initialization and bootup.



**Note:** Follow the BIOS Recovery instructions below for BIOS recovery when the main BIOS block crashes. However, when the BIOS Boot sector crashes, you will need to send the motherboard back to Supermicro for RMA repair.

#### To Recover the Main BIOS Block Using a USB-Attached Device

This feature allows the user to recover a BIOS image using a USB-attached device without additional utilities used. A USB flash device such as a USB Flash Drive, or

a USB CD/DVD ROM/RW device can be used for this purpose. However, a USB Hard Disk drive cannot be used for BIOS recovery at this time.

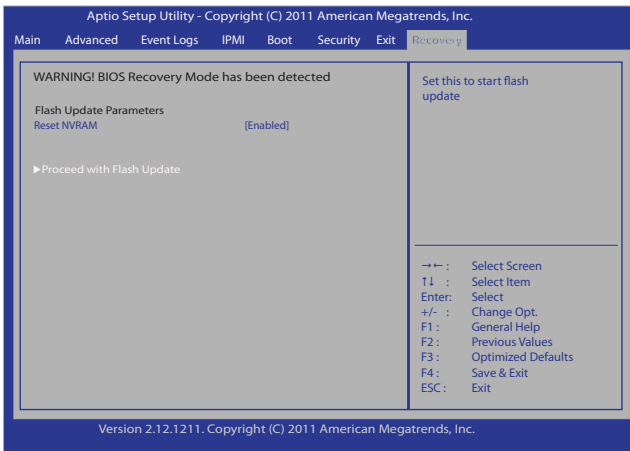
To perform UEFI BIOS recovery using a USB-attached device, follow the instructions below.

1. Using a different machine, copy the "Super.ROM" binary image file into the disc Root "\\" Directory of a USB device or a writeable CD/DVD.

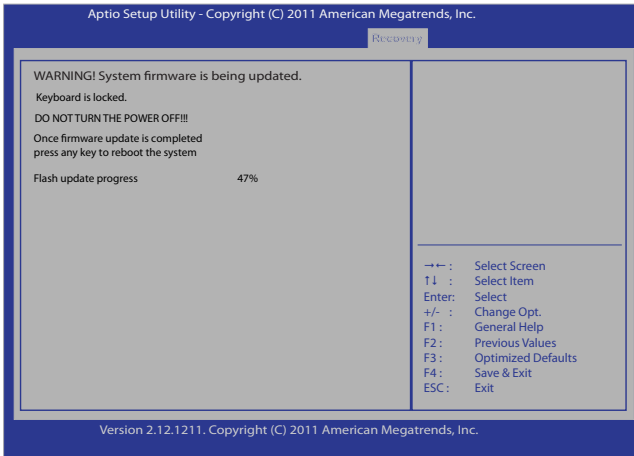


**Note:** If you cannot locate the "Super.ROM" file in your driver disk, visit our website at [www.supermicro.com](http://www.supermicro.com) to download the BIOS image into a USB flash device and rename it to "Super ROM" for BIOS recovery use.

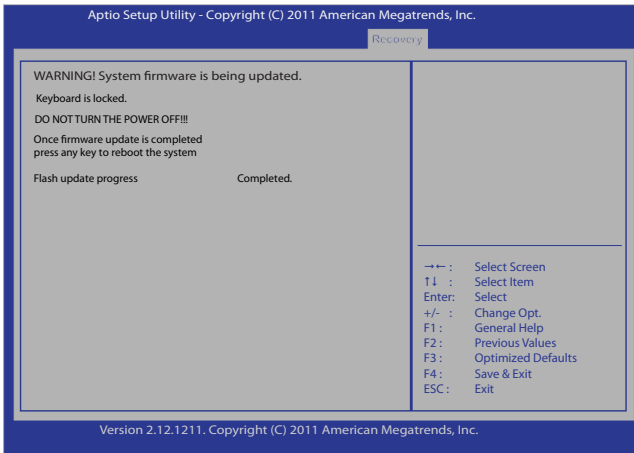
2. Insert the USB device that contains the new BIOS image ("Super.Rom") into your USB drive and power on the system
3. While powering on the system, keep pressing <Ctrl> and <Home> simultaneously on your PS2 or USB keyboard until your hear two short beeps. This may take from a few seconds to one minute.
4. After locating the new BIOS binary image, the system will enter the BIOS Recovery page as shown below.



**Note:** At this point, you may decide if you want to start with BIOS Recovery. If you decide to proceed with BIOS Recovery, follow the procedures below.



- When the screen as shown above displays, using the arrow key, select the item- "Proceed with flash update" and press the <Enter> key. You will see the progress of BIOS Recovery as shown in the screen below.



**Note:** *Do not interrupt* the process of BIOS flashing until it is completed.

- After the process of BIOS Recovery is complete, press any key to reboot the system.
- Using a different system, extract the BIOS package into a bootable USB flash drive.

8. When a DOS prompt appears, type AMI.BAT BIOSname.### at the prompt.



**Note:** Do not interrupt this process until BIOS flashing is completed.

9. After seeing the message that BIOS update is completed, unplug the AC power cable to clear CMOS, and then plug in the AC power cable to power on the system.
10. Press <Del> continuously to enter the BIOS Setup utility.
11. Press <F3> to load default settings.
12. After loading default settings, press <F4> to save the settings and exit the BIOS Setup utility.

(Disclaimer Continued)

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